

Model Name : GA-G41MT-D3

Revision 1.3

SHEET

TITLE

SHEET

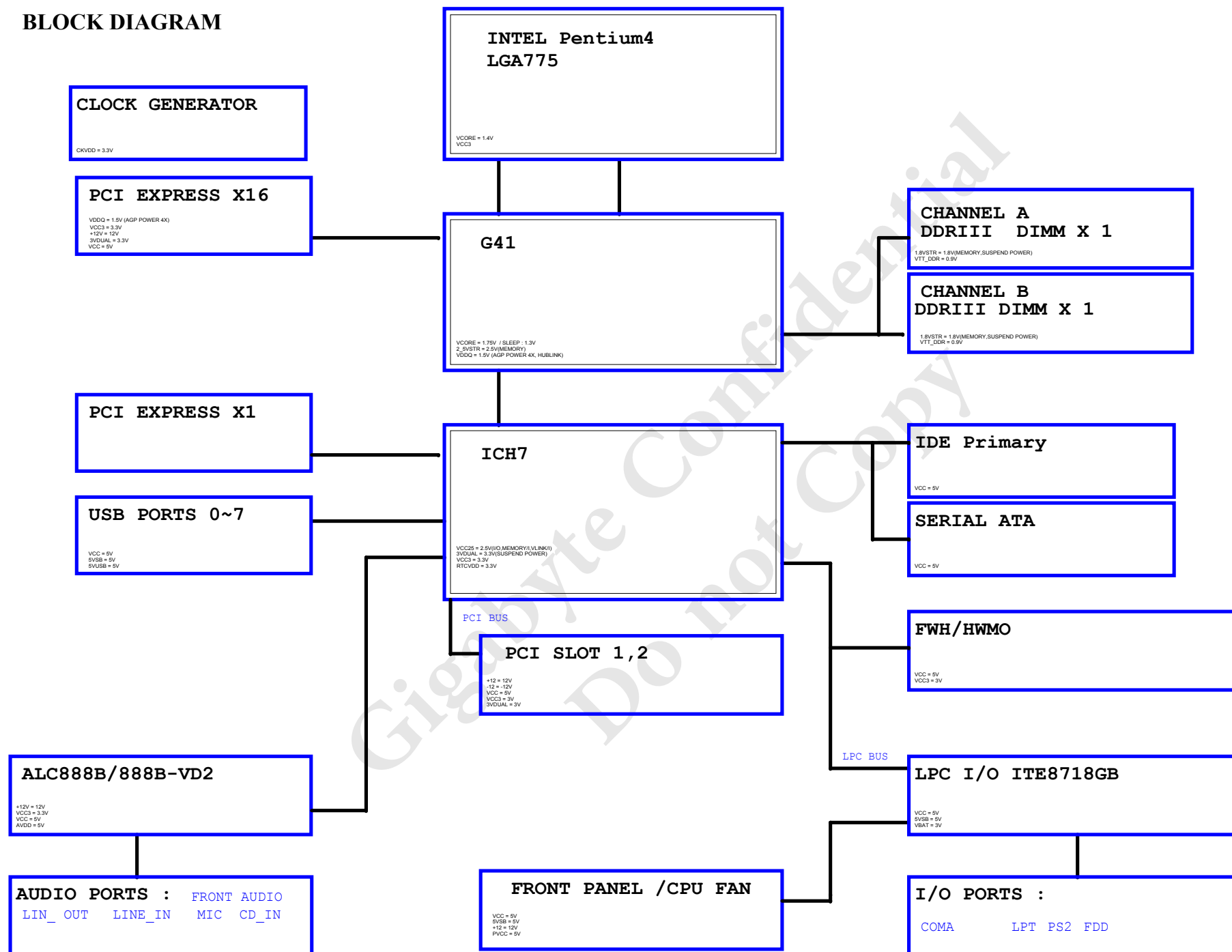
TITLE

01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4 LGA775 A
05	P4 LGA775 B,D
06	P4 LGA775 C
07	P4 LGA775 E,F,G,H
08	G41 HOST
09	G41 DDRII
10	G41 PCI E, DMI
11	G41 VGA
12	G41 GND
13	G41 PWR
14	PCI EXPRESS*16 SLOT
15	DDR3 CHANNEL A
16	DDR3 CHANNEL B
17	DDR3 TERMINATION
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	CK505 CLOCK.
22	PCI SLOT 1,2,PCIE*1
23	IDE/FLOPPY
24	ITE 8718 GB
25	COM LPT
26	CI, HWM, KB/MS, DUALBIOS
27	ALC888B/888B-VD2

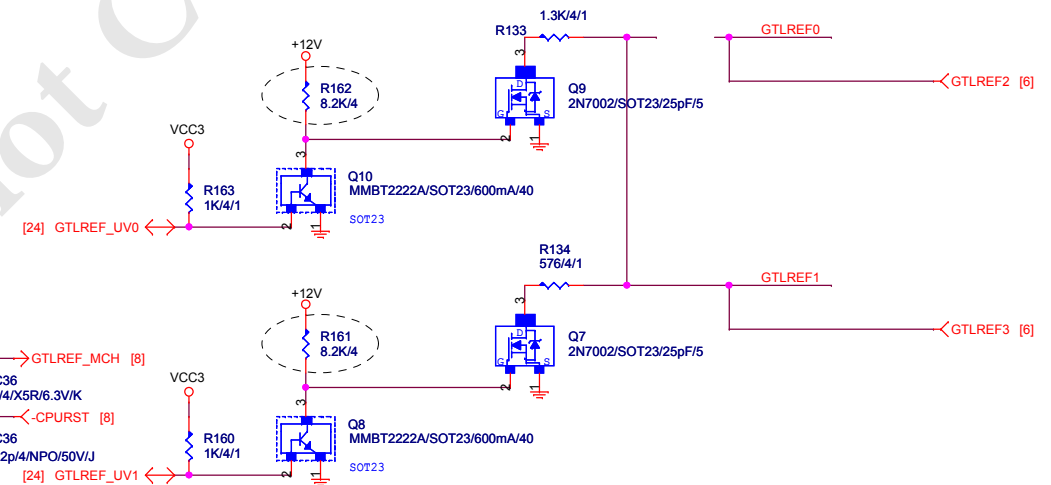
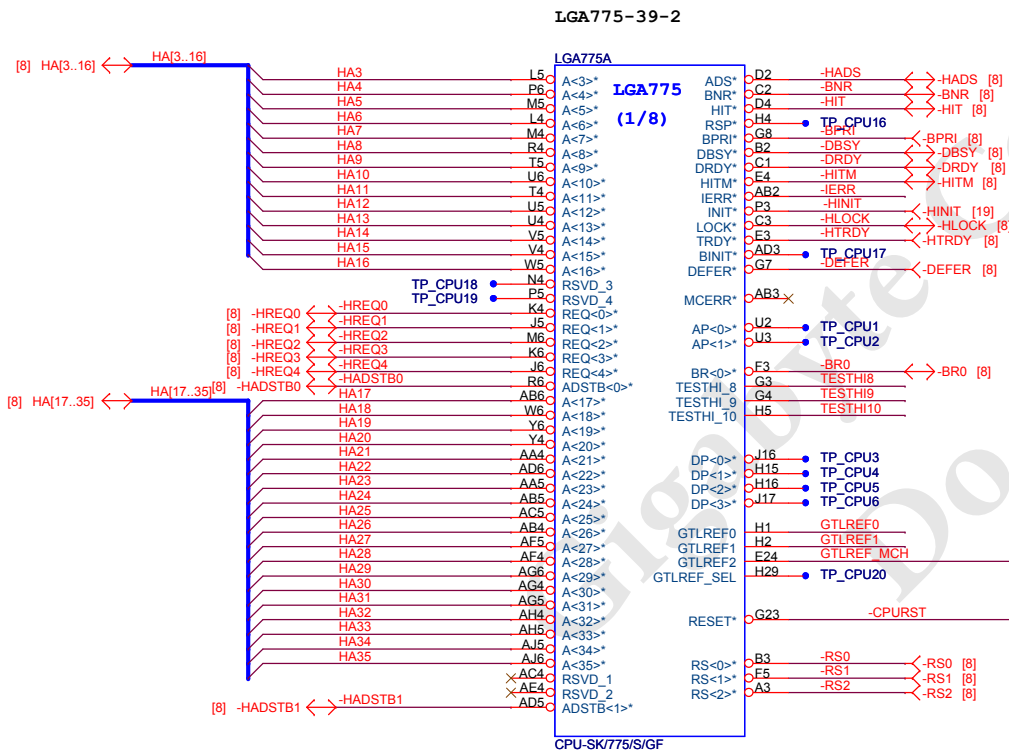
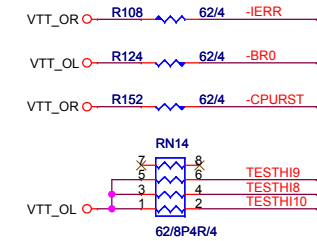
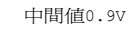
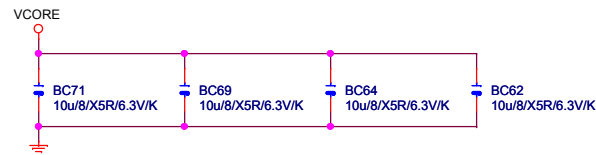
28	REAR AUDIO JACK
29	DISCRETE POWER
30	VCORE PWM ISL6312
31	ATX, OTHERS POWER
32	FRONT PANEL
33	REALTEK RTL8111E/8105E

<b>Gigabyte Technology</b>			
Title			
Cover Sheet			
Size	Document Number	GA-G41MT-D3	Rev
Custom			1.3
Date:	Thursday, April 29, 2010	Sheet	1 of 33

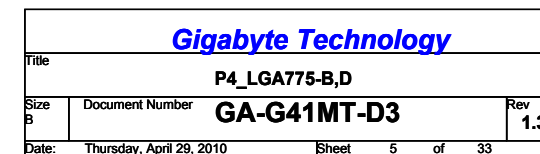
## BLOCK DIAGRAM





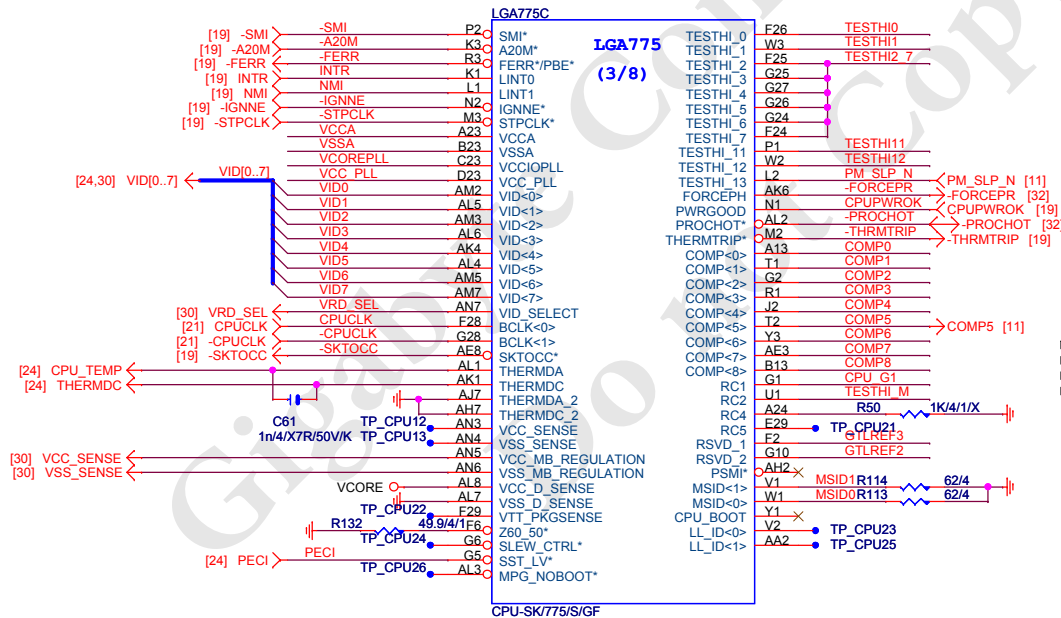
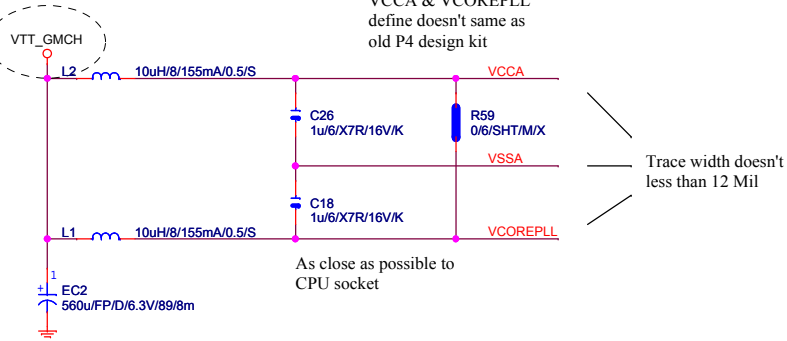
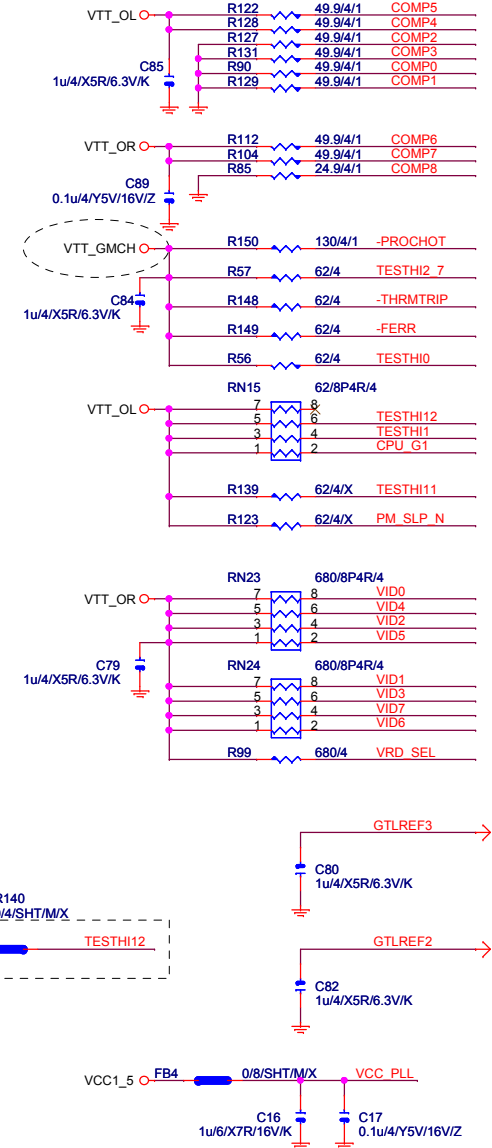


<b><i>Gigabyte Technology</i></b>			
Title			
<b>P4_LGA775-A</b>			
Size B	Document Number	<b>GA-G41MT-D3</b>	Rev <b>1.3</b>
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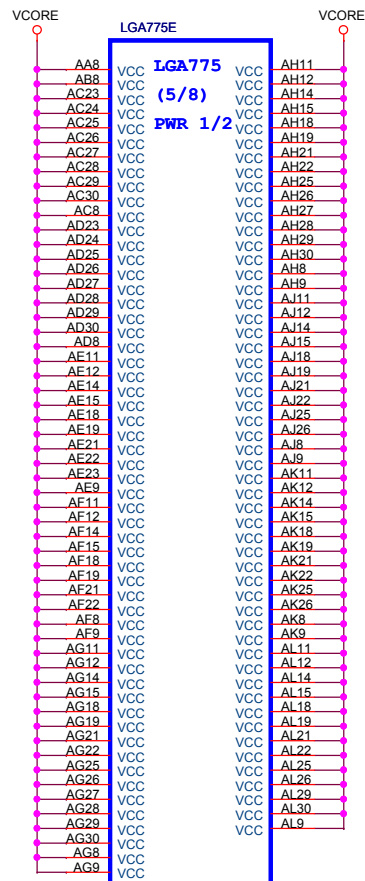
**Note:**

VCCA & VCOREPLL  
define doesn't same as  
old P4 design kit

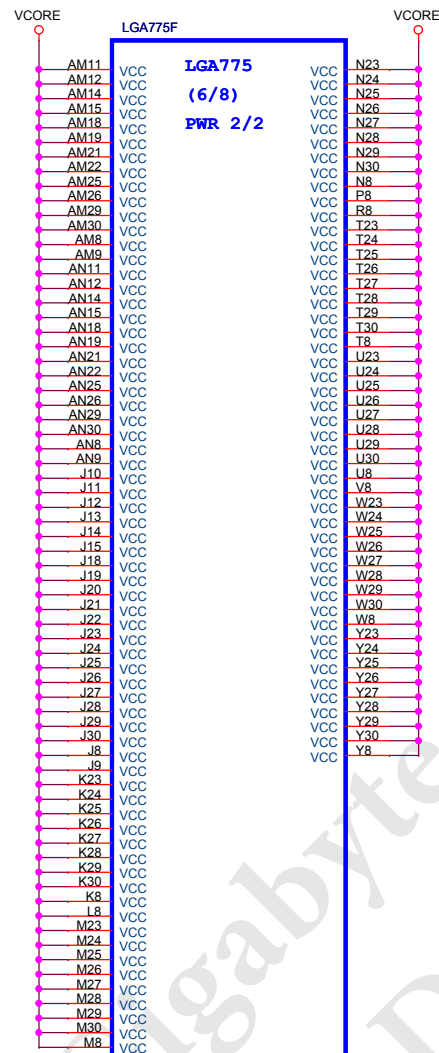
**Place outside of CPU socket****Gigabyte Technology**

Title			
P4_LGA775-C			
Size	Document Number	GA-G41MT-D3	
B		Rev 1.3	
Date:	Friday, April 30, 2010	Sheet	6 of 33

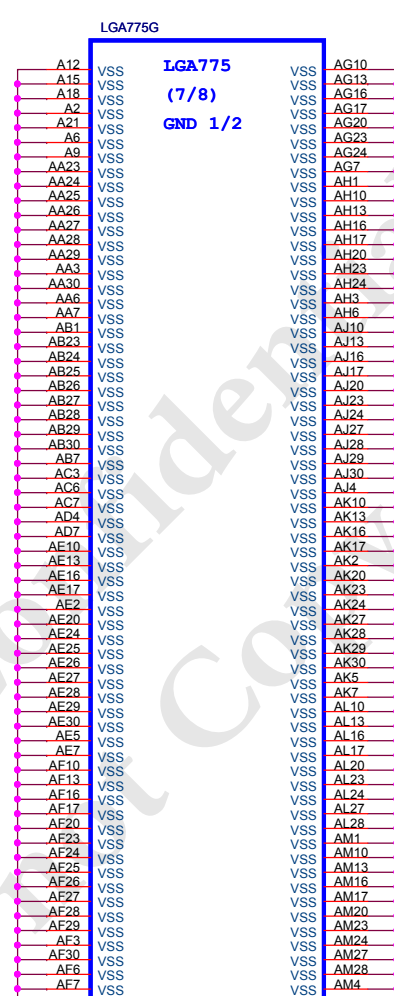
PECI:Platform Environment Control Interface



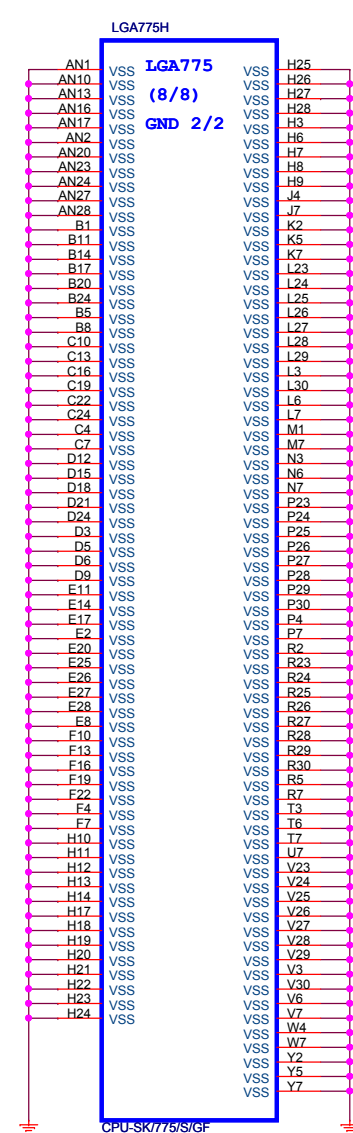
CPU-SK/775/S/GF



CPU-SK/775/S/GF



CPU-SK/775/S/GF



CPU-SK/775/S/GF

**Gigabyte Technology**

Title

**P4\_LGA775-E,F,G,H**

Size

Document Number

**GA-G41MT-D3**

Rev

**1.3**

Date:

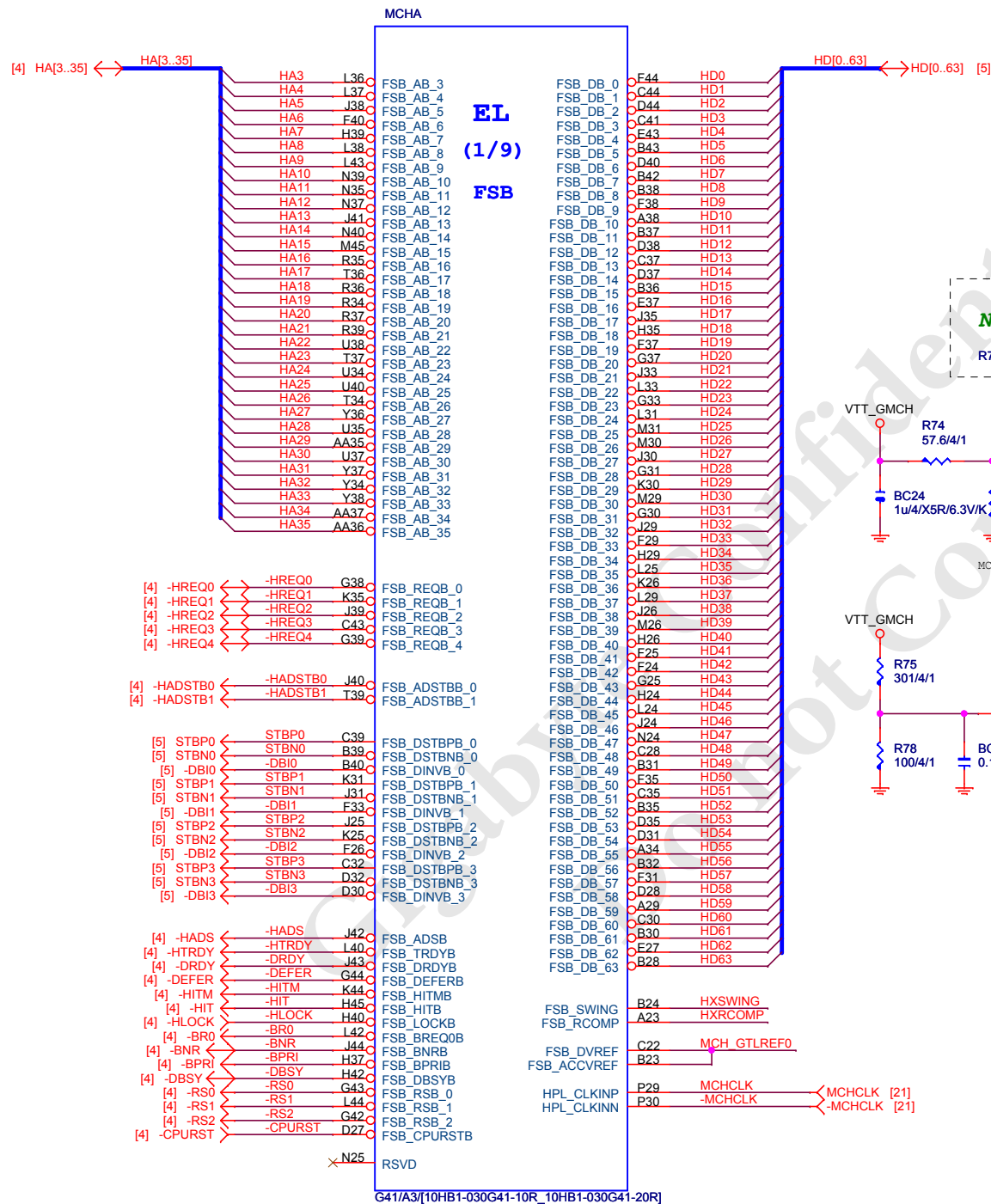
Thursday, April 29, 2010

Sheet

7

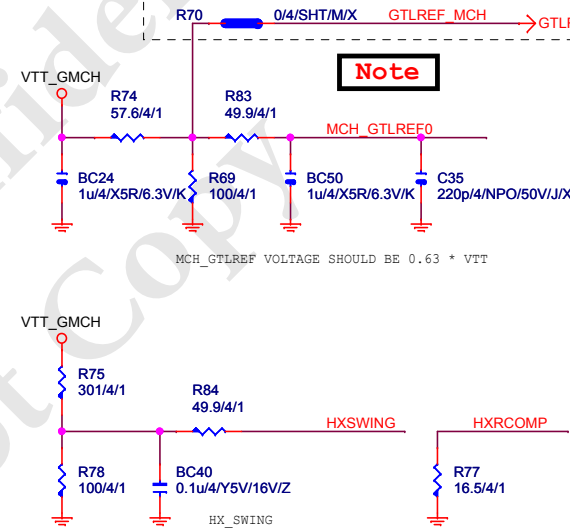
of

33



Not used for CoreTM2 Duo and Wolfdale

Note



Gigabyte Technology

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GMCH-HOST		
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MCHC

**EL**  
(3/9)  
**DDR\_A**

MAAA1 BC31	DDR_A_MA_0	BC5 DQSA0
MAAA2 BB32	DDR_A_MA_1	BD4 DQSA0
MAAA3 BC32	DDR_A_MA_2	BC3 DMA1
MAAA4 BD32	DDR_A_MA_4	BC2 MDA1
MAAA5 BB31	DDR_A_MA_5	BD3 MDA1
MAAA6 AV31	DDR_A_MA_6	BD7 MDA2
MAAA7 BA31	DDR_A_MA_7	BD7 MDA3
MAAA8 BD31	DDR_A_MA_8	BD2 MDA4
MAAA9 BD30	DDR_A_MA_9	BA3 MDA5
MAAA10 AW43	DDR_A_MA_10	BE6 MDA6
MAAA11 BC30	DDR_A_MA_11	
MAAA12 BB30	DDR_A_MA_12	BD6 MDA7
MAAA13 AM42	DDR_A_MA_13	BD9 DQSA1
MAAA14 BD28	DDR_A_MA_14	BC9 DQSA1
	DDR_A_DM_1	BD9 DMA1
[15] -SCASA ← SCASA AW42	DDR_A_WEB	BB8 MDA8
[15] -SRASA ← SRASA AW42	DDR_A_CASB	BA8 MDA8
	DDR_A_RASB	BD11 MDA10
	DDR_A_DM_10	BD11 MDA11
[15] SBA00 ← SBA00 AV45	DDR_A_BS_0	BD7 MDA12
[15] SBA11 ← SBA11 AY44	DDR_A_BS_1	BE8 MDA13
[15] SBA22 ← SBA22 BC28	DDR_A_BS_2	BD10 MDA14
[15] -CSA0 ← CSA0 AU43	DDR_A_CSB_0	AY11 MDA15
	DDR_A_CSB_1	
	DDR_A_CSB_2	BD15 DQSA2
	DDR_A_CSB_3	BB15 DQSA2
		BD14 DMA2
	DDR_A_DM_2	
[15] CKEA0 ← CKEA0 BB27	DDR_A_CKE_0	BB14 MDA16
[15] CKEA1 ← CKEA1 BB27	DDR_A_CKE_1	BC14 MDA17
	DDR_A_CKE_2	BC16 MDA18
	DDR_A_CKE_3	BB16 MDA19
	DDR_A_DM_19	BC15 MDA20
	DDR_A_DM_20	BE12 MDA21
[15] MODT_A0 ← MODT_A0 AR42	DDR_A_ODT_0	BA15 MDA22
[15] MODT_A1 ← MODT_A1 AM44	DDR_A_ODT_1	BD16 MDA23
	DDR_A_ODT_2	
	DDR_A_ODT_3	
		AR22 DQSA3
[15] DCLKA0 ← DCLKA0 AY37	DDR_A_CK_0	AT22 DQSA3
[15] -DCLKA0 ← DCLKA0 BA37	DDR_A_CK_0	AV22 DMA3
	DDR_A_CK_1	
	DDR_A_CK_2	AW21 MDA24
[15] DCLKA2 ← DCLKA2 AY37	DDR_A_CK_2	AY22 MDA25
[15] -DCLKA2 ← DCLKA2 AU37	DDR_A_CK_3	AY24 MDA27
	DDR_A_CK_4	AY24 MDA28
	DDR_A_CK_5	AY24 MDA29
	DDR_A_CK_6	AT21 MDA30
	DDR_A_CK_7	AR24 MDA31
	DDR_A_CK_8	AU24 MDA31
	DDR_A_CK_9	
	DDR_A_DM_4	AH43 DQSA4
	DDR_A_DM_4	AH42 DQSA4
		AK42 DMA4
	DDR_A_DM_32	AL41 MDA32
	DDR_A_DM_33	AK43 MDA33
	DDR_A_DM_34	AG42 MDA34
	DDR_A_DM_35	AG44 MDA35
	DDR_A_DM_36	AL42 MDA36
	DDR_A_DM_37	AG44 MDA37
	DDR_A_DM_38	AG44 MDA38
	DDR_A_DM_39	AG41 MDA39
	DDR_A_DM_5	AD43 DQSA5
	DDR_A_DM_5	AE42 DQSA5
	DDR_A_DM_5	AE45 DMA5
	DDR_A_DM_40	AF43 MDA40
	DDR_A_DM_41	AF42 MDA41
	DDR_A_DM_42	AC44 MDA42
	DDR_A_DM_43	AC42 MDA43
	DDR_A_DM_44	AF40 MDA44
	DDR_A_DM_45	AT44 MDA45
	DDR_A_DM_46	AD44 MDA46
	DDR_A_DM_47	AC41 MDA47
	DDR_A_DM_6	Y43 DQSA6
	DDR_A_DM_6	T42 DQSA6
	DDR_A_DM_6	AM45 DMA6
	DDR_A_DM_48	AB43 MDA48
	DDR_A_DM_49	AA42 MDA49
	DDR_A_DM_50	WA42 MDA50
	DDR_A_DM_51	WA41 MDA51
	DDR_A_DM_52	AB42 MDA52
	DDR_A_DM_53	AB44 MDA53
	DDR_A_DM_54	Y44 MDA54
	DDR_A_DM_55	Y40 MDA55
	DDR_A_DM_7	T44 DQSA7
	DDR_A_DM_7	T43 DQSA7
	DDR_A_DM_7	T42 DMA7
	DDR_A_DM_56	Y42 MDA56
	DDR_A_DM_57	U45 MDA57
	DDR_A_DM_58	R40 MDA58
	DDR_A_DM_59	P44 MDA59
	DDR_A_DM_60	V44 MDA60
	DDR_A_DM_61	V43 MDA61
	DDR_A_DM_62	R41 MDA62
	DDR_A_DM_63	R44 MDA63

G41A3[10H81-030G41-10R\_10H81-030G41-20R]

MDA0.63	← MDA0.63	[15]
MAAA0.14	← MAAA0.14	[15]
MODT_A0.1	← MODT_A0.1	[15]
DQSA0.7	← DQSA0.7	[15]
DQSA0.7	← DQSA0.7	[15]
DMA0.7	← DMA0.7	[15]

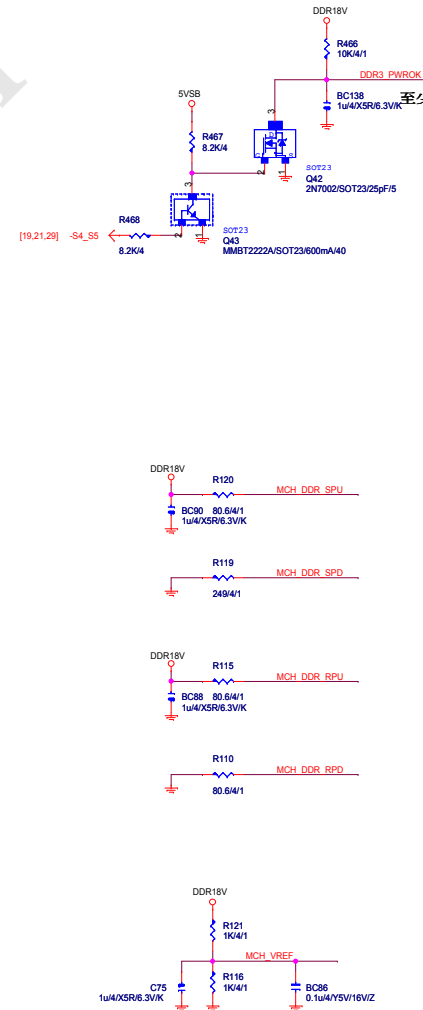
MCHD

**EL**  
(4/9)  
**DDR\_B**

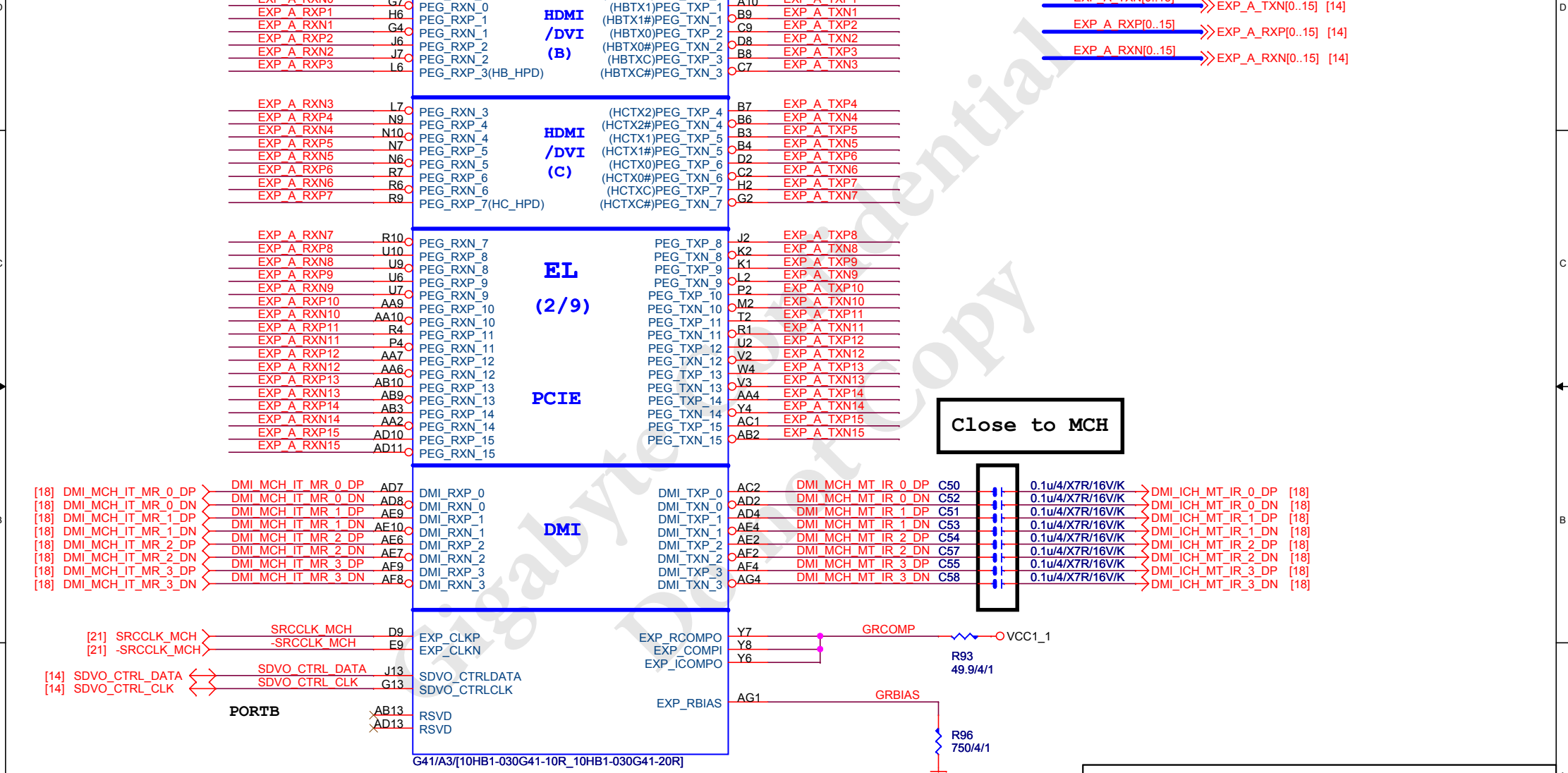
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MAAB1 BB23	DDR_B_MA_1	AW9 DQSB0
MAAB2 BB24	DDR_B_MA_2	AY6 DMB0
MAAB3 BD23	DDR_B_MA_3	
MAAB4 BB22	DDR_B_MA_4	AV7 MDB0
MAAB5 BD22	DDR_B_MA_5	AW4 MDB1
MAAB6 BC22	DDR_B_MA_6	BA9 MDB2
MAAB7 BC20	DDR_B_MA_7	AU11 MDB3
MAAB8 BD20	DDR_B_MA_8	AU7 MDB5
MAAB9 BD20	DDR_B_MA_9	AU8 MDB5
MAAB10 BC26	DDR_B_MA_10	AW7 MDB6
MAAB11 BD19	DDR_B_MA_11	AY9 MDB7
MAAB12 BB19	DDR_B_MA_12	
MAAB13 BE38	DDR_B_MA_13	
MAAB14 BA19	DDR_B_MA_14	
	DDR_B_DM_1	AT15 DQSB1
	DDR_B_DM_1	AU15 DQSB1
	DDR_B_DM_1	AR15 DMB1
	DDR_B_DM_8	AY13 MDB8
	DDR_B_DM_9	AP15 MDB9
	DDR_B_DM_10	AW15 MDB10
	DDR_B_DM_11	AT16 MDB11
	DDR_B_DM_12	AU13 MDB12
	DDR_B_DM_13	AW13 MDB13
	DDR_B_DM_14	AP16 MDB14
	DDR_B_DM_15	MD15 MDB15
	DDR_B_DM_2	AR20 DQSB2
	DDR_B_DM_2	AR17 DQSB2
	DDR_B_DM_2	AU17 DMB2
	DDR_B_DM_16	AY17 MDB16
	DDR_B_DM_17	AV17 MDB17
	DDR_B_DM_18	AR21 MDB18
	DDR_B_DM_19	AV20 MDB19
	DDR_B_DM_20	AP17 MDB20
	DDR_B_DM_21	AW16 MDB21
	DDR_B_DM_22	AT20 MDB22
	DDR_B_DM_23	AN20 MDB23
	DDR_B_DM_3	AU26 DQSB3
	DDR_B_DM_3	AT26 DQSB3
	DDR_B_DM_3	AV25 DMB3
	DDR_B_DM_24	AT25 MDB24
	DDR_B_DM_25	AV26 MDB25
	DDR_B_DM_26	AU29 MDB26
	DDR_B_DM_27	AV29 MDB27
	DDR_B_DM_28	AW25 MDB28
	DDR_B_DM_29	AR26 MDB29
	DDR_B_DM_30	MD30 MDB30
	DDR_B_DM_31	AR29 MDB31
	DDR_B_DM_3	AR38 DQSB4
	DDR_B_DM_4	AR37 DQSB4
	DDR_B_DM_4	AU39 DMB4
	DDR_B_DM_32	AR36 MDB32
	DDR_B_DM_33	AU38 MDB33
	DDR_B_DM_34	AN35 MDB34
	DDR_B_DM_35	AN37 MDB35
	DDR_B_DM_36	AV39 MDB36
	DDR_B_DM_37	AN39 MDB37
	DDR_B_DM_38	AU40 MDB38
	DDR_B_DM_39	AU41 MDB39
	DDR_B_DM_5	AK34 DQSB5
	DDR_B_DM_5	AL34 DQSB5
	DDR_B_DM_5	AL37 DMB5
	DDR_B_DM_40	AL35 MDB40
	DDR_B_DM_41	AL36 MDB41
	DDR_B_DM_42	AK36 MDB42
	DDR_B_DM_43	AJ34 MDB43
	DDR_B_DM_44	AN39 MDB44
	DDR_B_DM_45	AN40 MDB45
	DDR_B_DM_46	AK37 MDB46
	DDR_B_DM_47	AL39 MDB47
	DDR_B_DM_6	AE37 DQSB6
	DDR_B_DM_6	AE36 DQSB6
	DDR_B_DM_6	AJ35 DMB6
	DDR_B_DM_48	AJ38 MDB48
	DDR_B_DM_49	AJ37 MDB49
	DDR_B_DM_50	AE38 MDB50
	DDR_B_DM_51	AE37 MDB51
	DDR_B_DM_52	AK40 MDB52
	DDR_B_DM_53	AK40 MDB53
	DDR_B_DM_54	AE34 MDB54
	DDR_B_DM_55	AE35 MDB55
	DDR_B_DM_56	AB35 DQSB7
	DDR_B_DM_57	AD35 DQSB7
	DDR_B_DM_7	AD37 DMB7
	DDR_B_DM_58	AD40 MDB56
	DDR_B_DM_59	AD38 MDB57
	DDR_B_DM_60	AB40 MDB58
	DDR_B_DM_61	AE39 MDB59
	DDR_B_DM_62	AE36 MDB60
	DDR_B_DM_63	AE39 MDB61
	DDR_B_DM_64	AB37 MDB62
	DDR_B_DM_65	AB38 MDB63
	DDR3_A_CSB1	
	DDR3_A_MA0	
	DDR3_B_ODT3	
	DDR3_DRAM_PWROK	
	DDR3_DRAM_RSTB	
[15] -CSA1 ← CSA1 AR43	DDR3_A_CSB1	
[15] -SWEA ← SWEA AT44	DDR3_A_WEB	
	DDR3_B_ODT3	
[15,16] DDR3_RST ←	DDR3_DRAM_PWROK	
	DDR3_DRAM_RSTB	
		AN29 RSVD
		AN30 RSVD
		AJ33 RSVD
		AK33 RSVD
	MCH_VREF	BB44
	MCH_DDR_RPD	AY42
	MCH_DDR_RPU	BA43
	MCH_DDR_SPD	BC43
	MCH_DDR_SPU	BC44

G41A3[10H81-030G41-10R\_10H81-030G41-20R]


MD80.63	← MD80.63	[16]
MAAB0.14	← MAAB0.14	[16]
MODT_B0.1	← MODT_B0.1	[16]
DQSB0.7	← DQSB0.7	[16]
DQSB0.7	← DQSB0.7	[16]
DM80.7	← DM80.7	[16]



Gigabyte Technology



Close to MCH

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Title			
GMCH-PCI E & DMI			
Size Custom	Document Number		Rev
	GA-G41MT-D3		1.3
Date:	Thursday, April 29, 2010	Sheet	10 of 33
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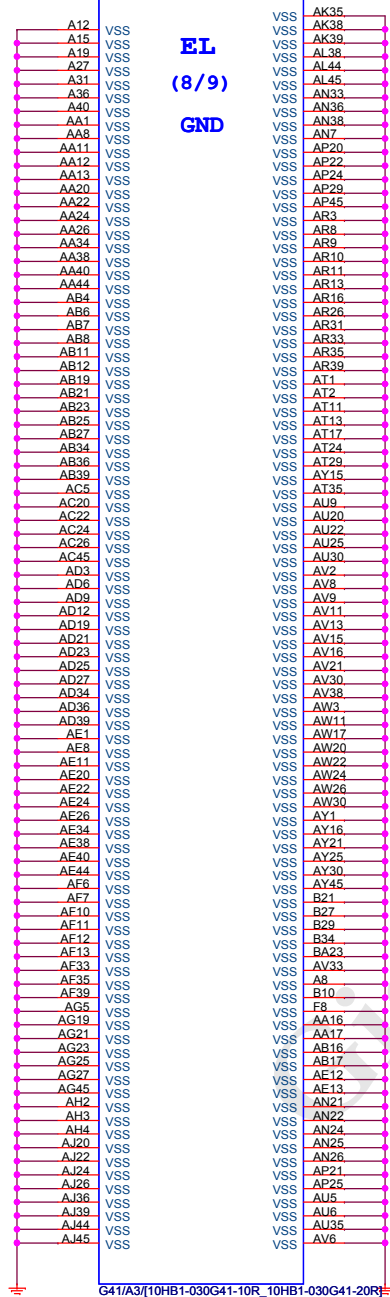
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Size B	Document Number		Rev
	GA-G41MT-D3		1.3
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Size B	Document Number <b>GA-G41MT-D3</b>	Rev <b>1.3</b>
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Date:	Wednesday, May 12, 2010	Sheet	11	of	33
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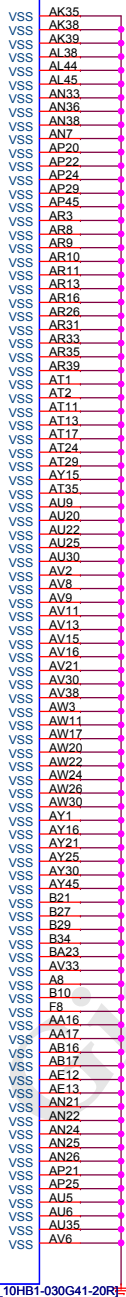
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MCHH



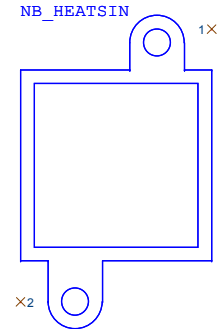
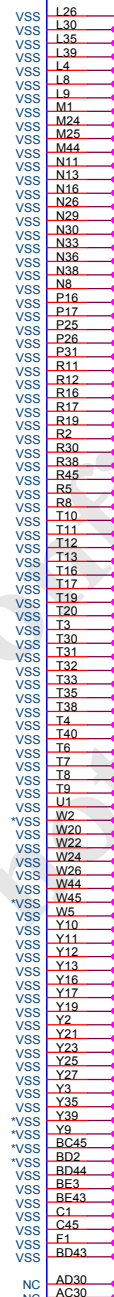
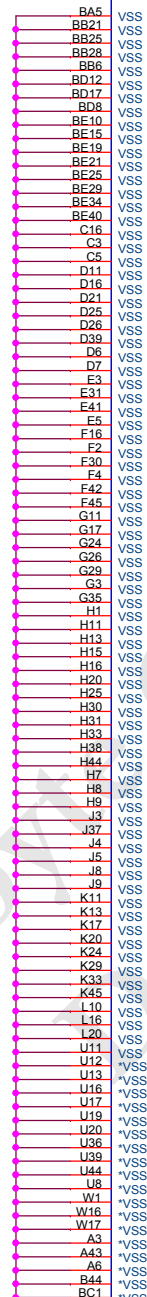
G41/A3[10HB1-030G41-10R\_10HB1-030G41-20R]

EL  
(8/9)  
GND



G41/A3[10HB1-030G41-10R\_10HB1-030G41-20R]

EL  
(9/9)  
GND



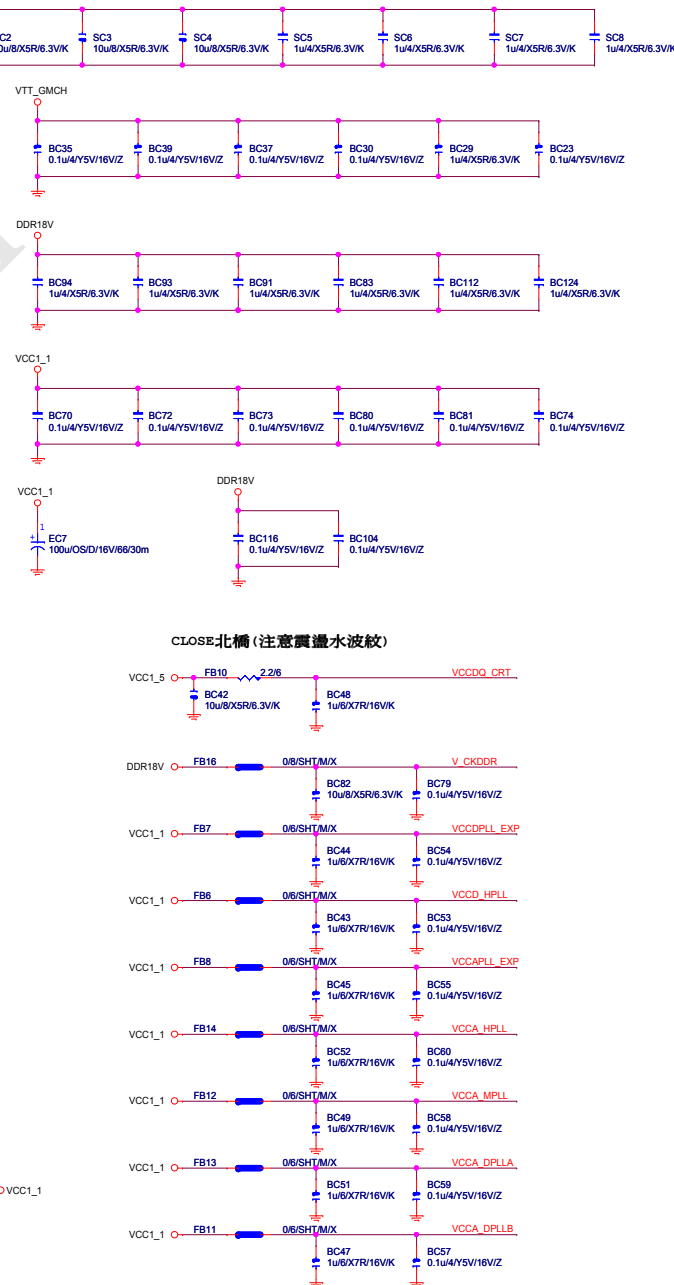
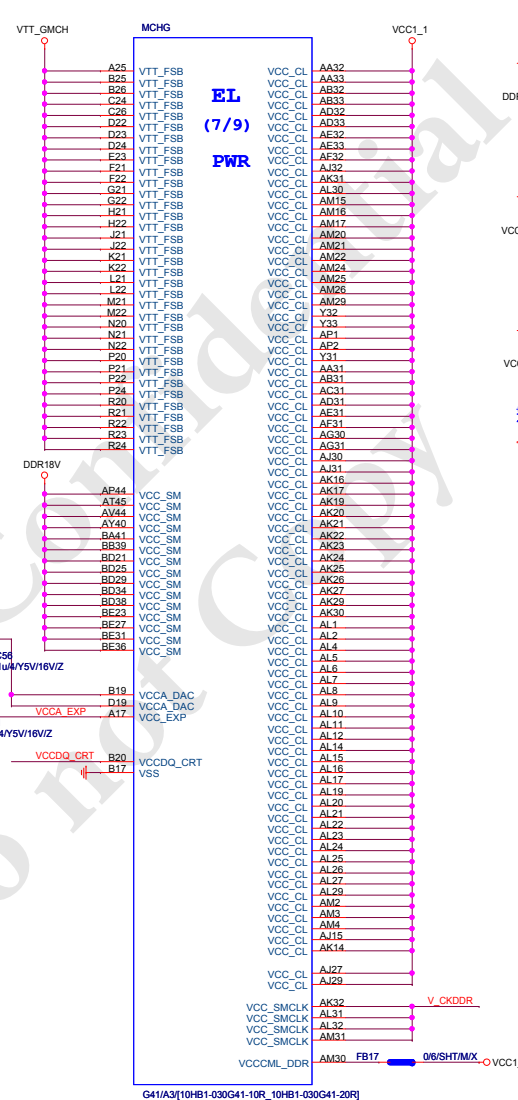
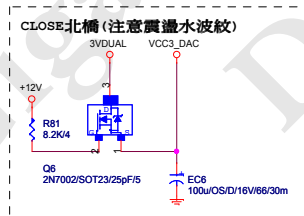
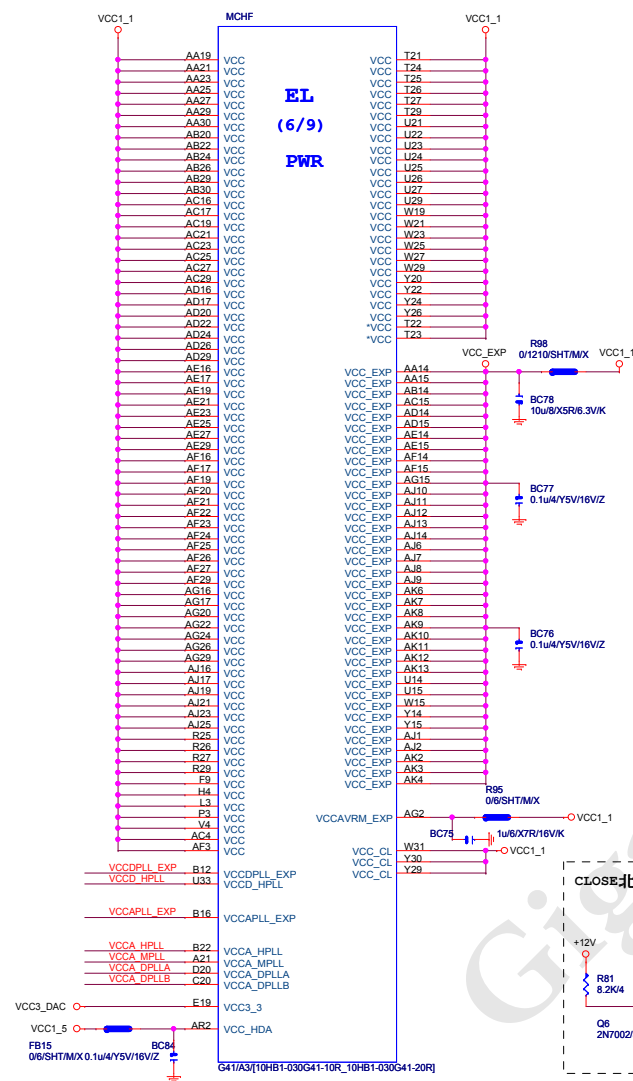
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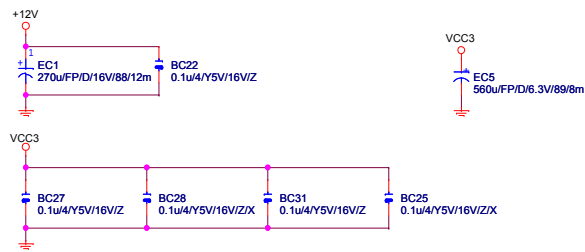
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BGASINK445A-L GRAY 20MM

**Gigabyte Technology**

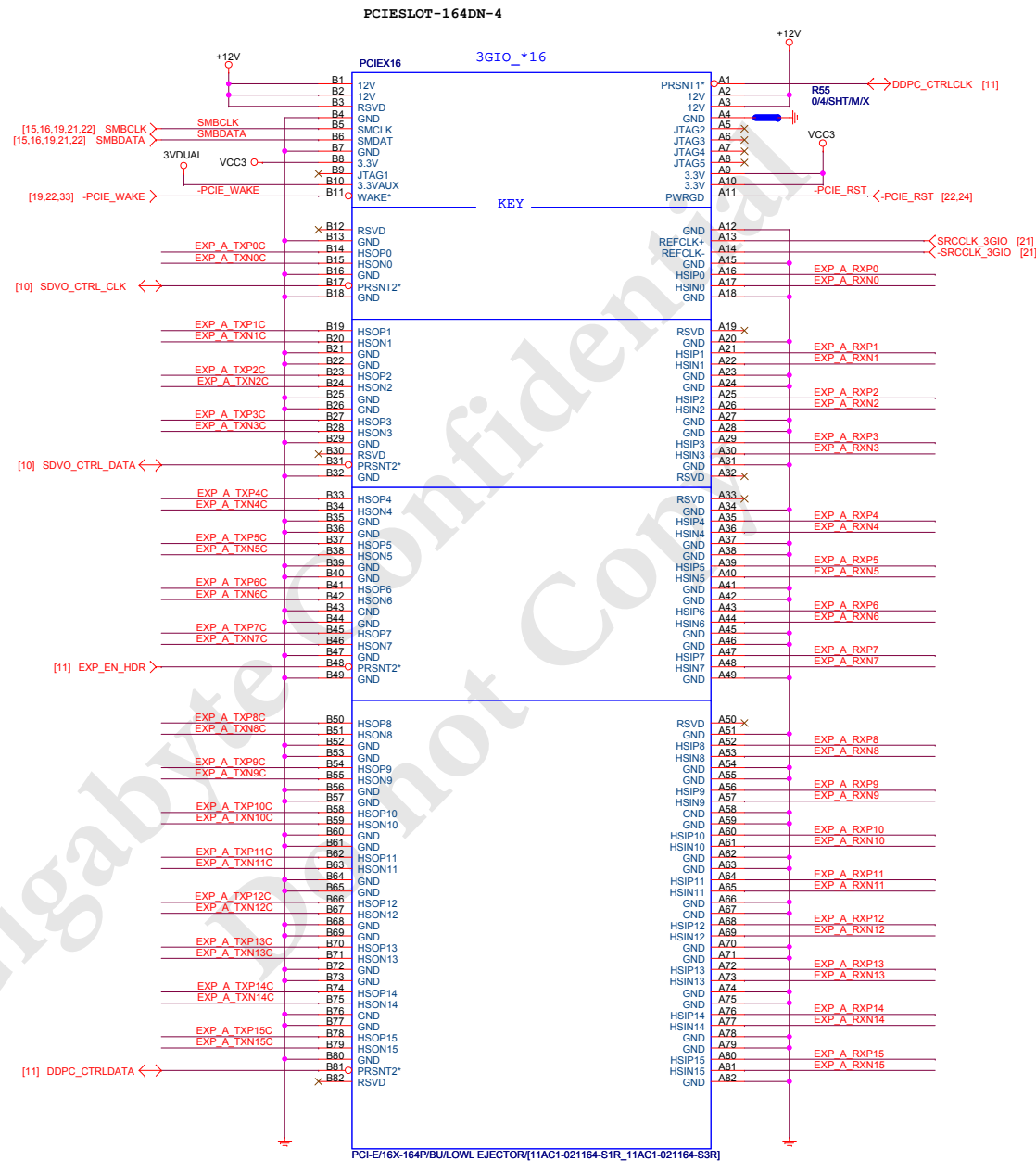
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GMCH-GND			
Size	Document Number	Rev	
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EXP\_A\_TXP[0..15] >>> EXP\_A\_TXP[0..15] [10] EXP\_A\_RXP[0..15] >>> EXP\_A\_RXP[0..15] [10]  
 EXP\_A\_TXN[0..15] >>> EXP\_A\_TXN[0..15] [10] EXP\_A\_RXN[0..15] >>> EXP\_A\_RXN[0..15] [10]

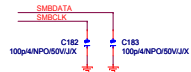
EXP_A_TXP0	C30	0.1u4/X7R/16V/K	EXP_A_TXP0C
EXP_A_TXN0	C29	0.1u4/X7R/16V/K	EXP_A_TXN0C
EXP_A_TXP1	C31	0.1u4/X7R/16V/K	EXP_A_TXP1C
EXP_A_TXN1	C32	0.1u4/X7R/16V/K	EXP_A_TXN1C
EXP_A_TXP2	C33	0.1u4/X7R/16V/K	EXP_A_TXP2C
EXP_A_TXN2	C34	0.1u4/X7R/16V/K	EXP_A_TXN2C
EXP_A_TXP3	C37	0.1u4/X7R/16V/K	EXP_A_TXP3C
EXP_A_TXN3	C38	0.1u4/X7R/16V/K	EXP_A_TXN3C
EXP_A_TXP4	C39	0.1u4/X7R/16V/K	EXP_A_TXP4C
EXP_A_TXN4	C40	0.1u4/X7R/16V/K	EXP_A_TXN4C
EXP_A_TXP5	C42	0.1u4/X7R/16V/K	EXP_A_TXP5C
EXP_A_TXN5	C41	0.1u4/X7R/16V/K	EXP_A_TXN5C
EXP_A_TXP6	C44	0.1u4/X7R/16V/K	EXP_A_TXP6C
EXP_A_TXN6	C43	0.1u4/X7R/16V/K	EXP_A_TXN6C
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EXP_A_TXP8	C48	0.1u4/X7R/16V/K	EXP_A_TXP8C
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EXP_A_TXN10	C62	0.1u4/X7R/16V/K	EXP_A_TXN10C
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EXP_A_TXN11	C64	0.1u4/X7R/16V/K	EXP_A_TXN11C
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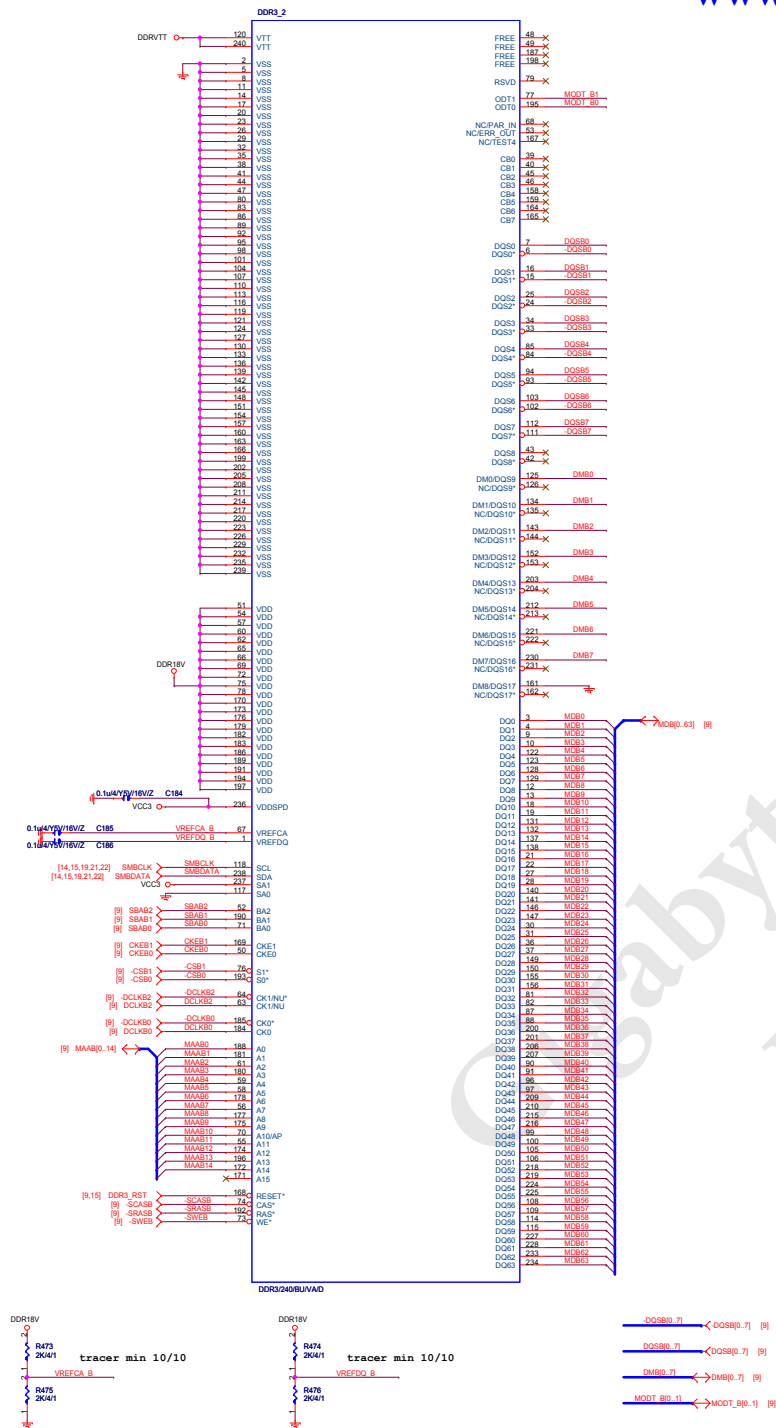


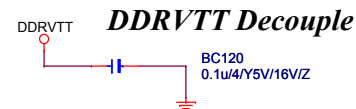
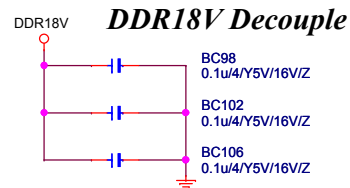
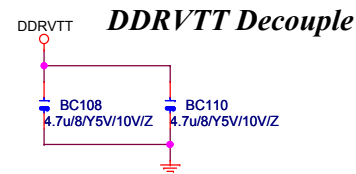
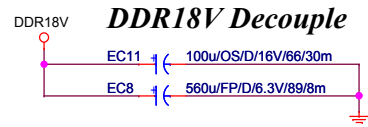
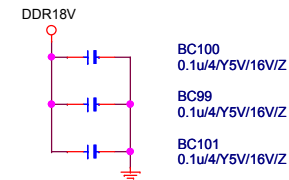
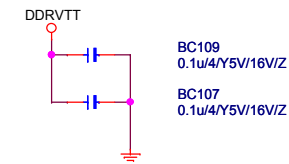
LOWV LEFT BLUE

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**DDR TERMINATION  
CHANNEL A****DDR TERMINATION  
CHANNEL B****DDR18V Decouple****DDRVTT Decouple****Gigabyte Technology**

Title

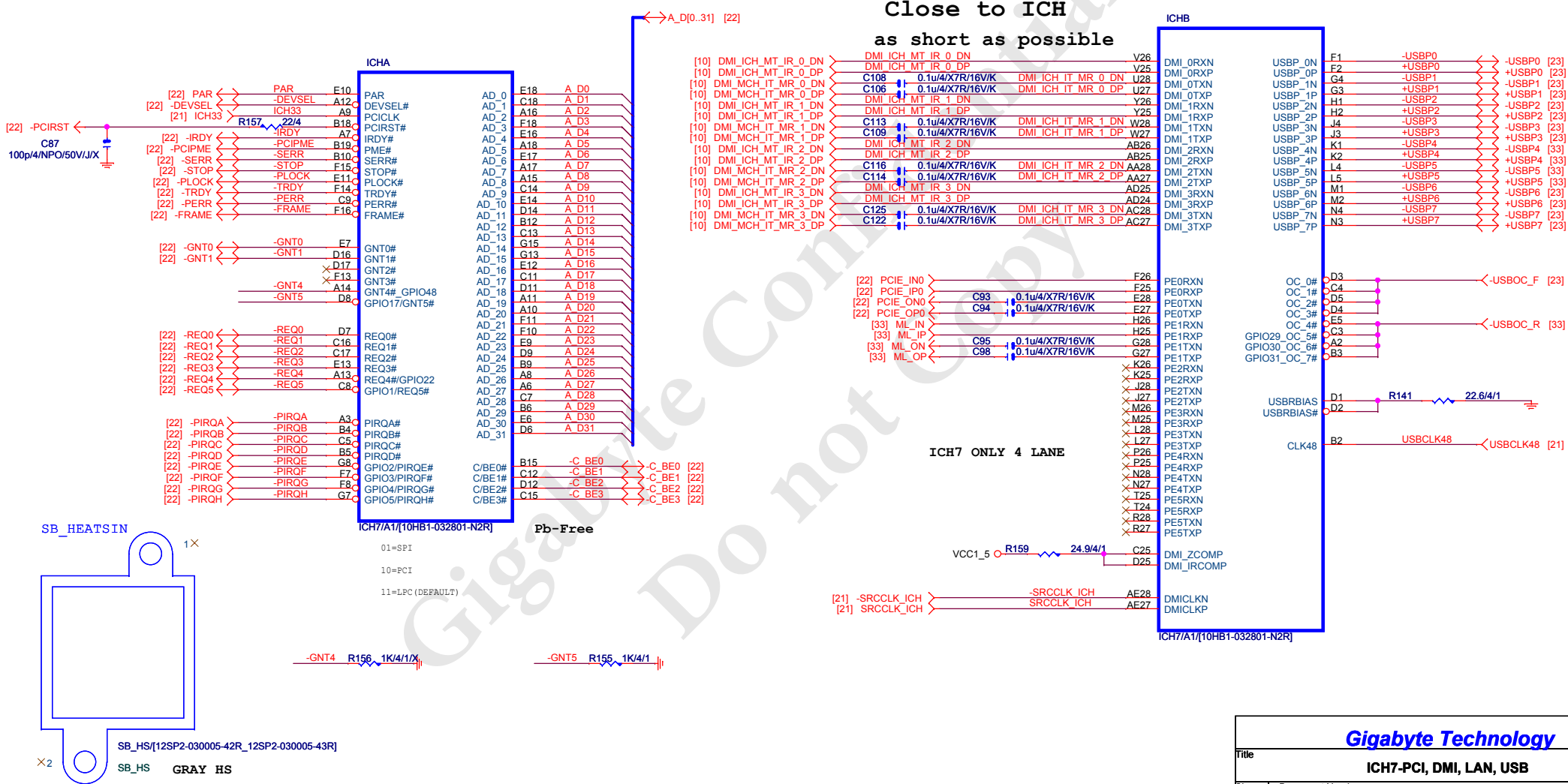
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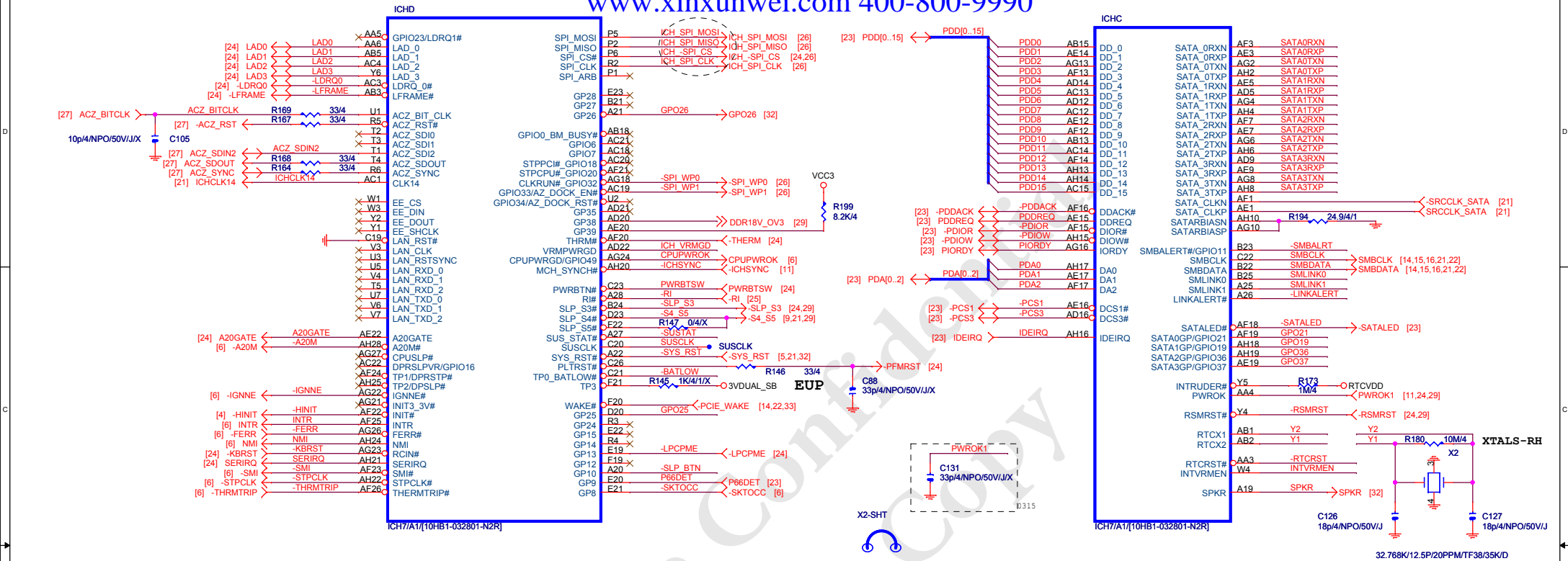
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**1.3**

Date: Friday, April 30, 2010

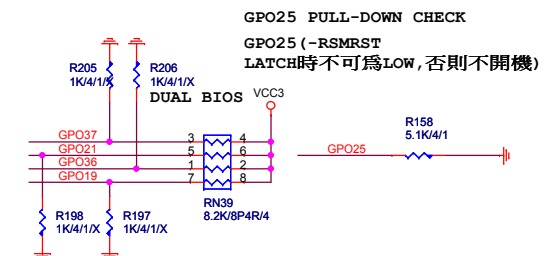
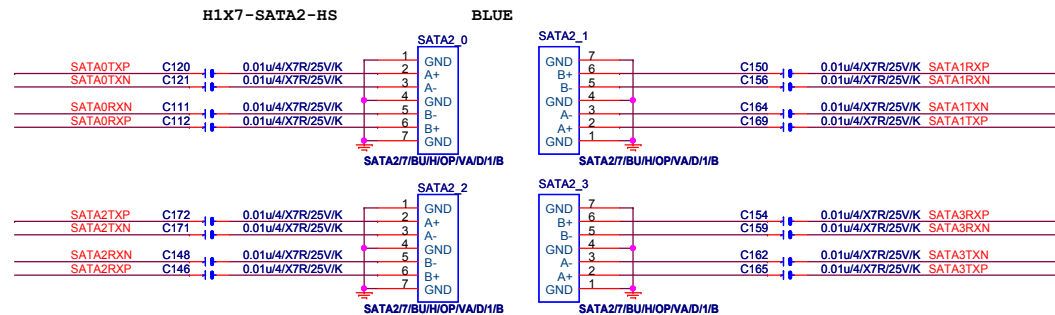
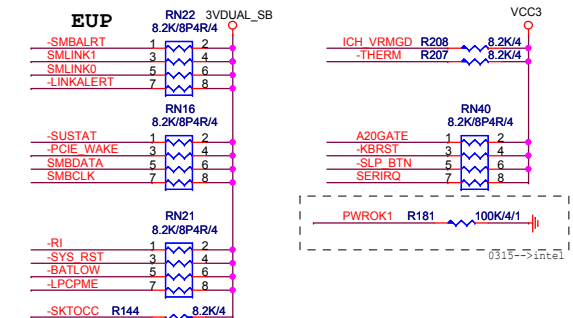
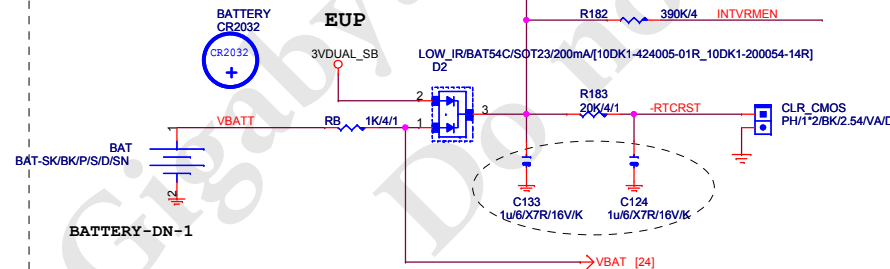
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## NEW TYPE: BATTERY-DUAL-4

RB 不可放在BATTERY  
SOCKET下方



## Gigabyte Technology

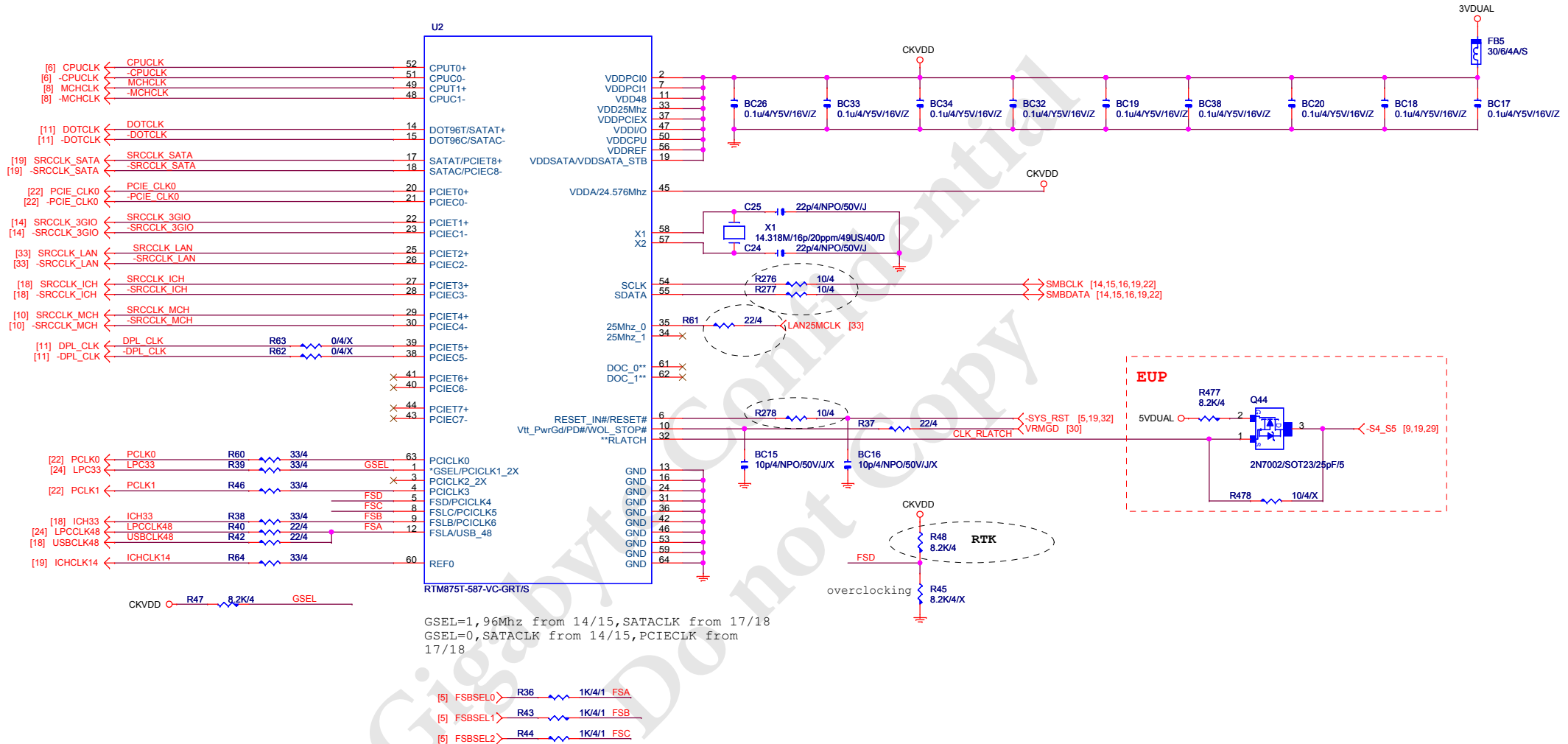
## ICH7-IDE, SATA, GPIO, CTRL

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Title

CK505 CLK GEN

Size  
Custom

Document Number

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Date:

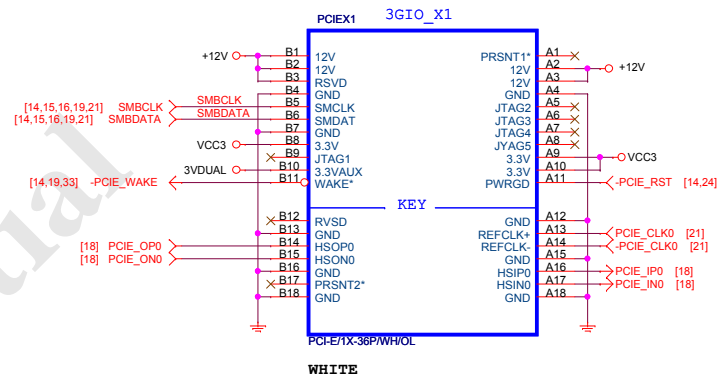
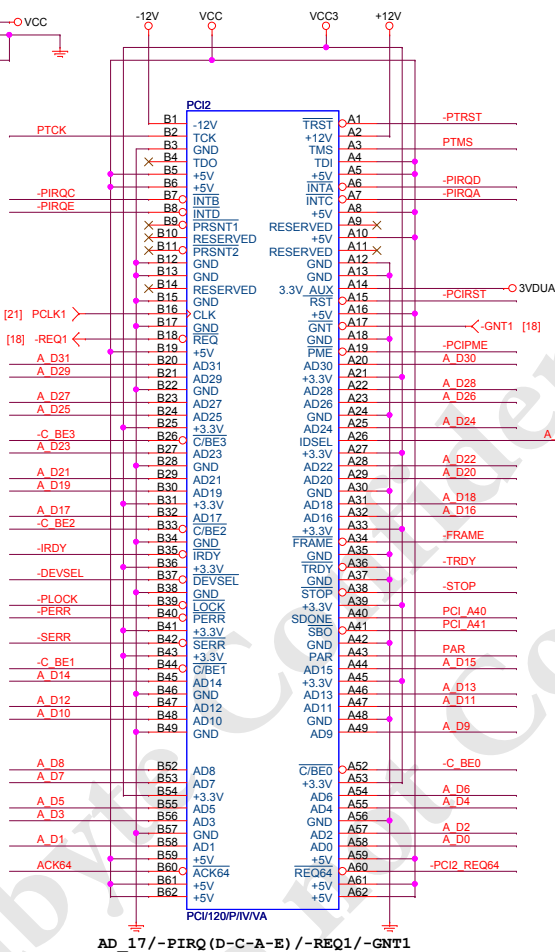
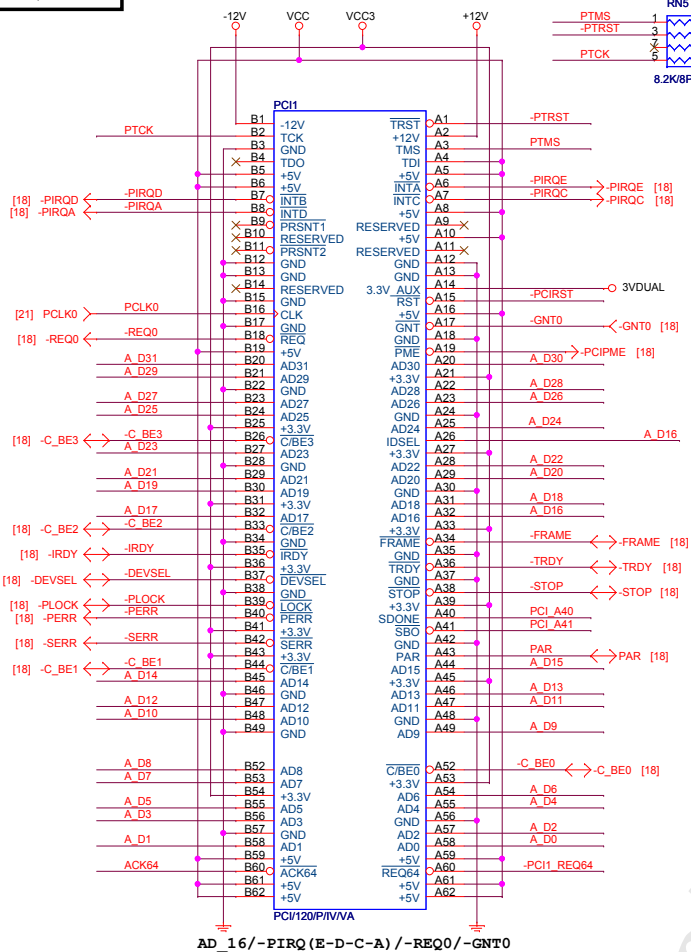
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PCI1, 2 SLOT

PCI1\*1

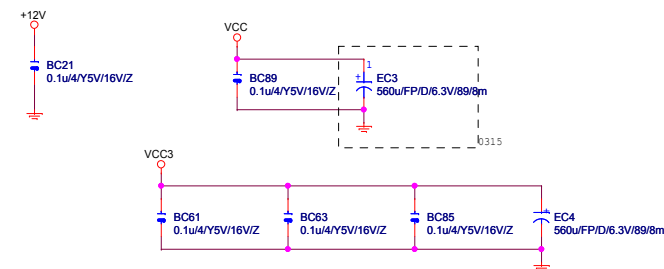
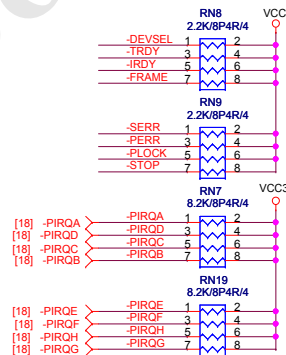
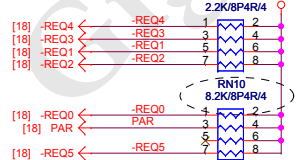


[18] A\_D[0..31] ↔ A\_D[0..31]

-PCIRST ↔ -PCIRST [18]

C28  
33p4/NPO/50V/J/X

Place close to PCI1



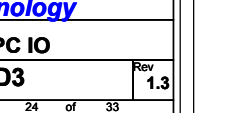
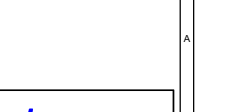
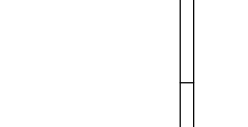
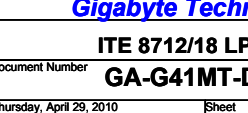
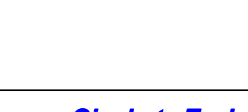
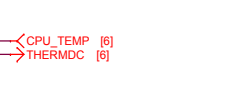
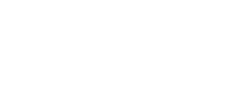
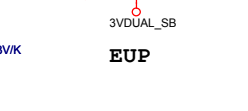
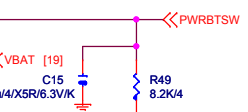
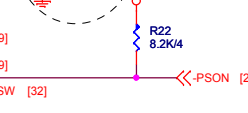
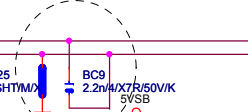
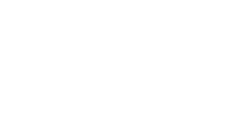
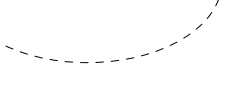
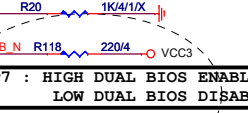
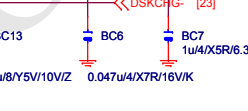
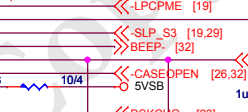
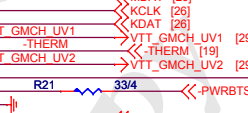
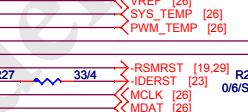
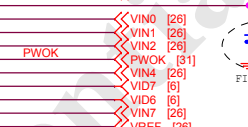
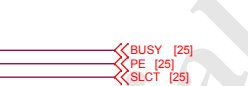
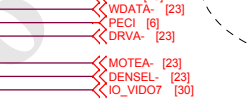
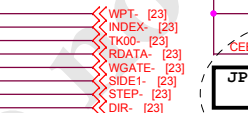
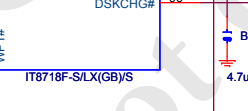
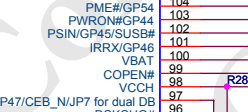
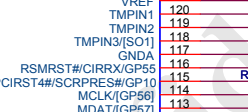
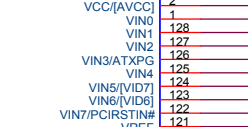
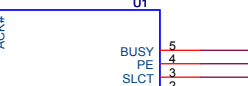
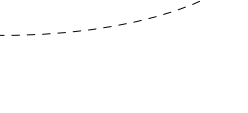
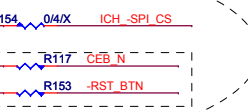
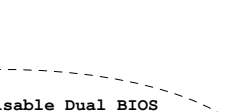
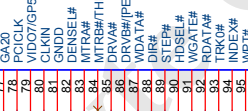
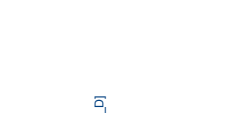
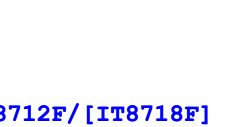
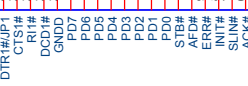
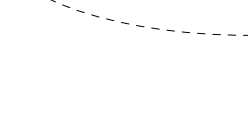
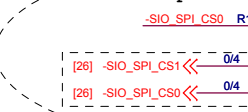
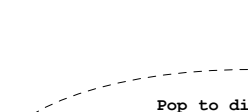
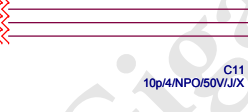
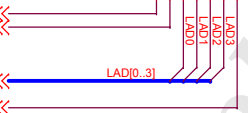
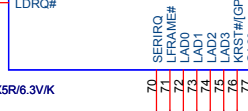
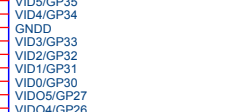
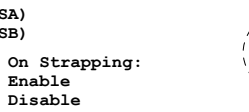
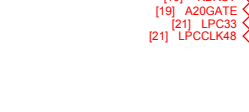
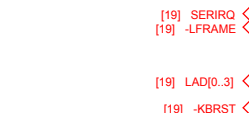
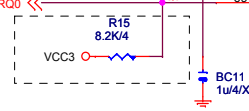
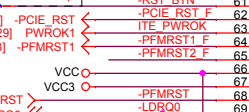
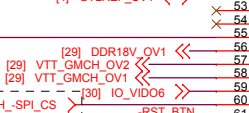
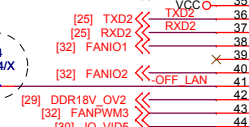
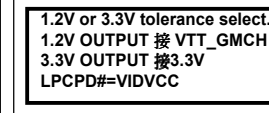
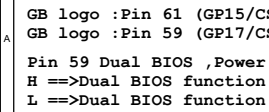
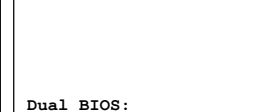
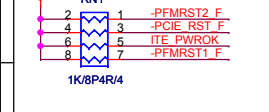
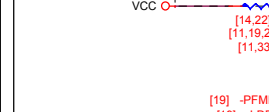
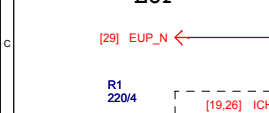
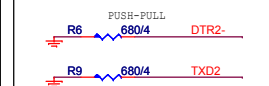
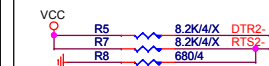
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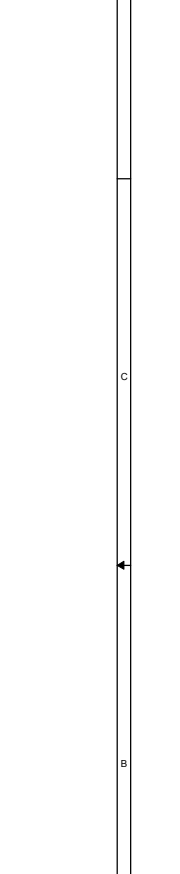
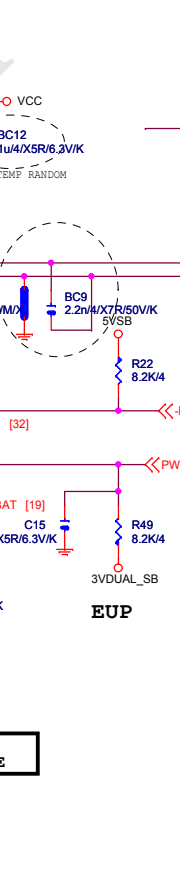
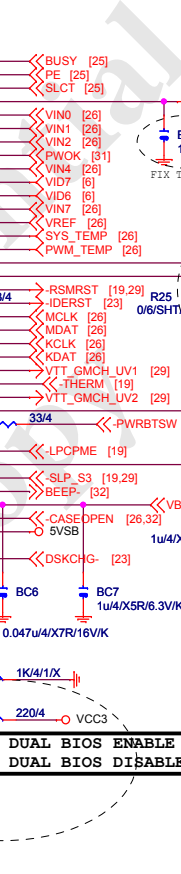
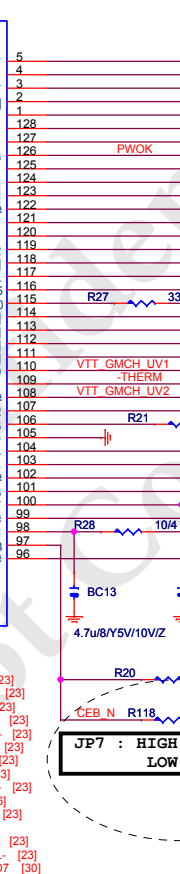
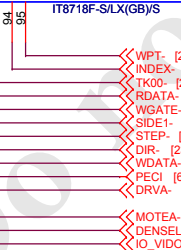


RTS2- ==LOW CPU FAN 50%  
==HIGH 100%

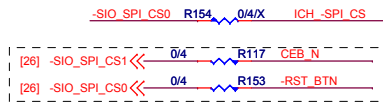
DEFAULT 50%



IT8712F/[IT8718F]

Dual BIOS:  
GB logo :Pin 61 (GP15/CSA)  
GB logo :Pin 59 (GP17/CSB)Pin 59 Dual BIOS ,Power On Strapping:  
H ==>Dual BIOS function Enable  
L ==>Dual BIOS function Disable1.2V or 3.3V tolerance select.  
1.2V OUTPUT 接 VTT\_GMCH  
3.3V OUTPUT 接 3.3V  
LPCPD# =VIDVCC

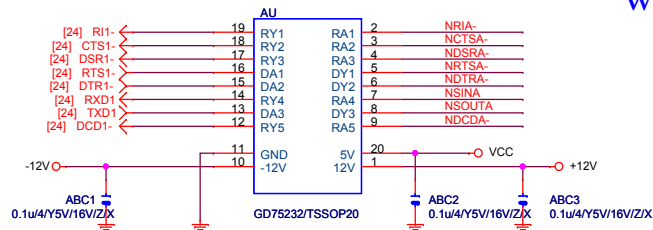
Pop to disable Dual BIOS



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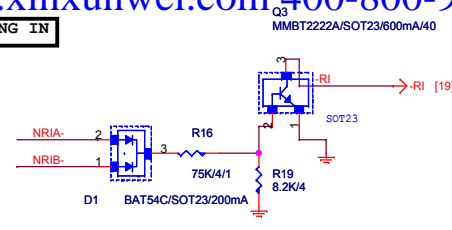
Title			ITE 8712/18 LPC IO	
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## COMA

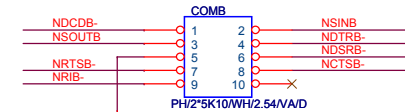
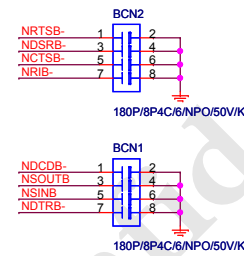
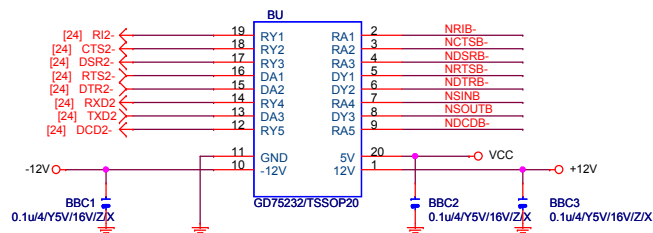
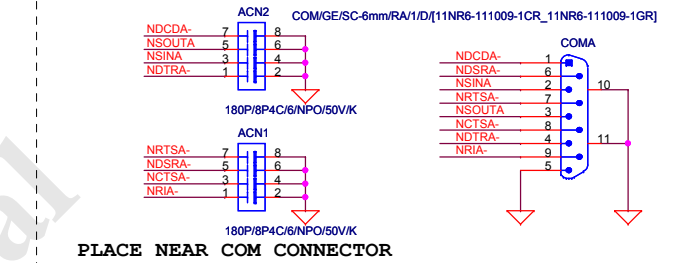


IC20TSSOP-1

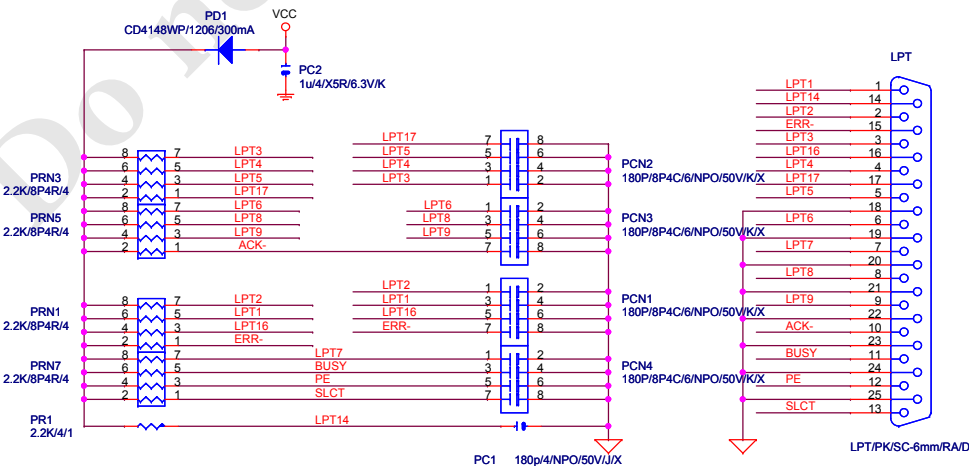
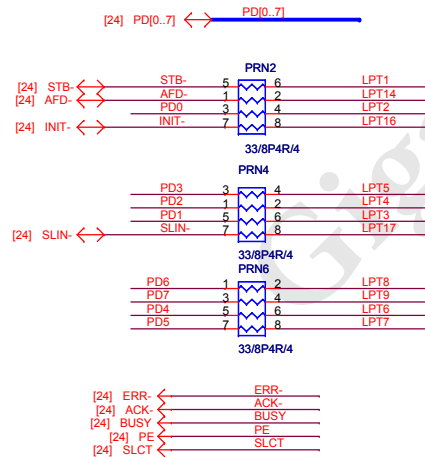
## RING IN



## EXTERNAL COMA



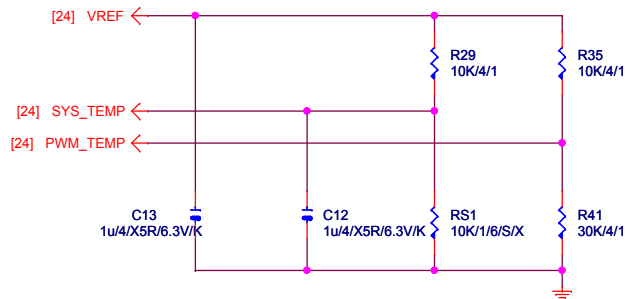
## LPT PORT



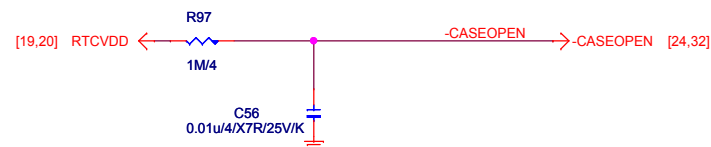
Gigabyte Technology

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COM & LPT PORT		
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## TEMP H/W MONITOR

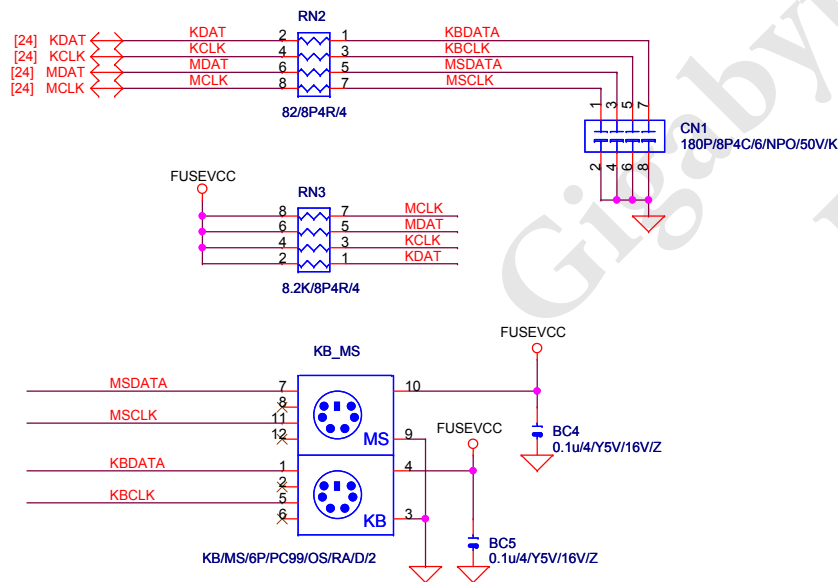


## CASE OPEN

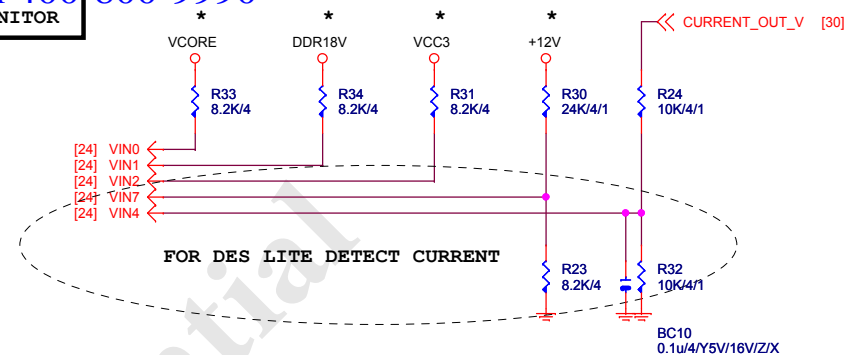


## Case Open Circuits

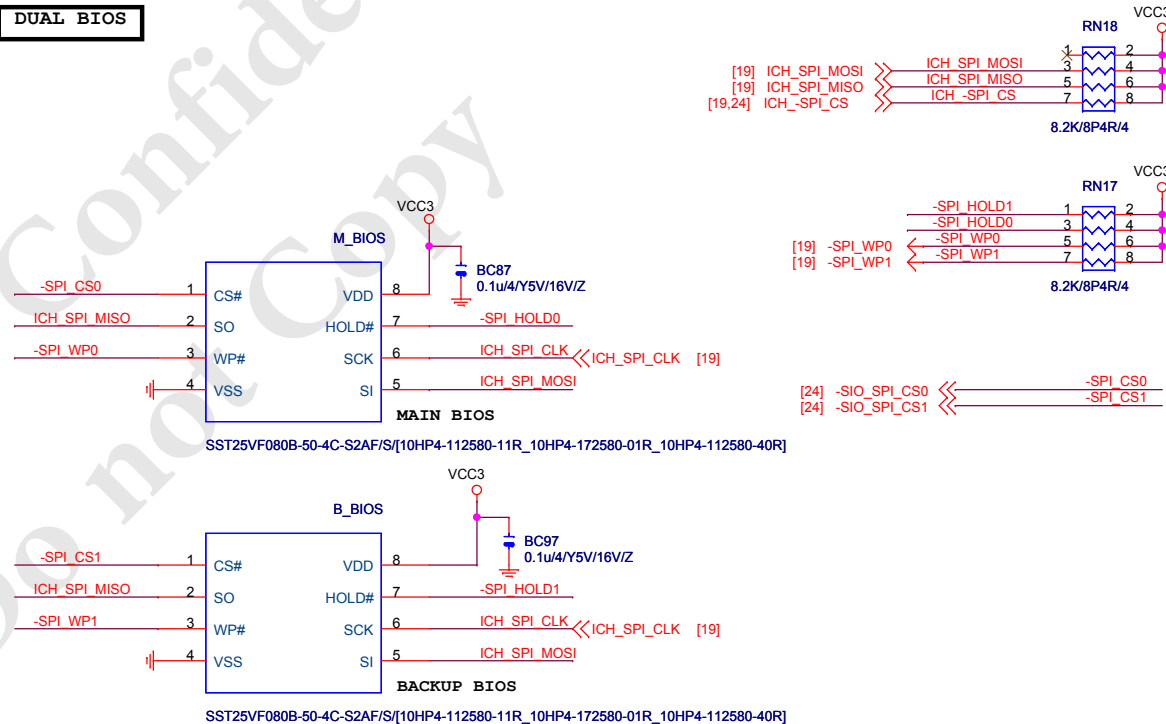
## KB/MS

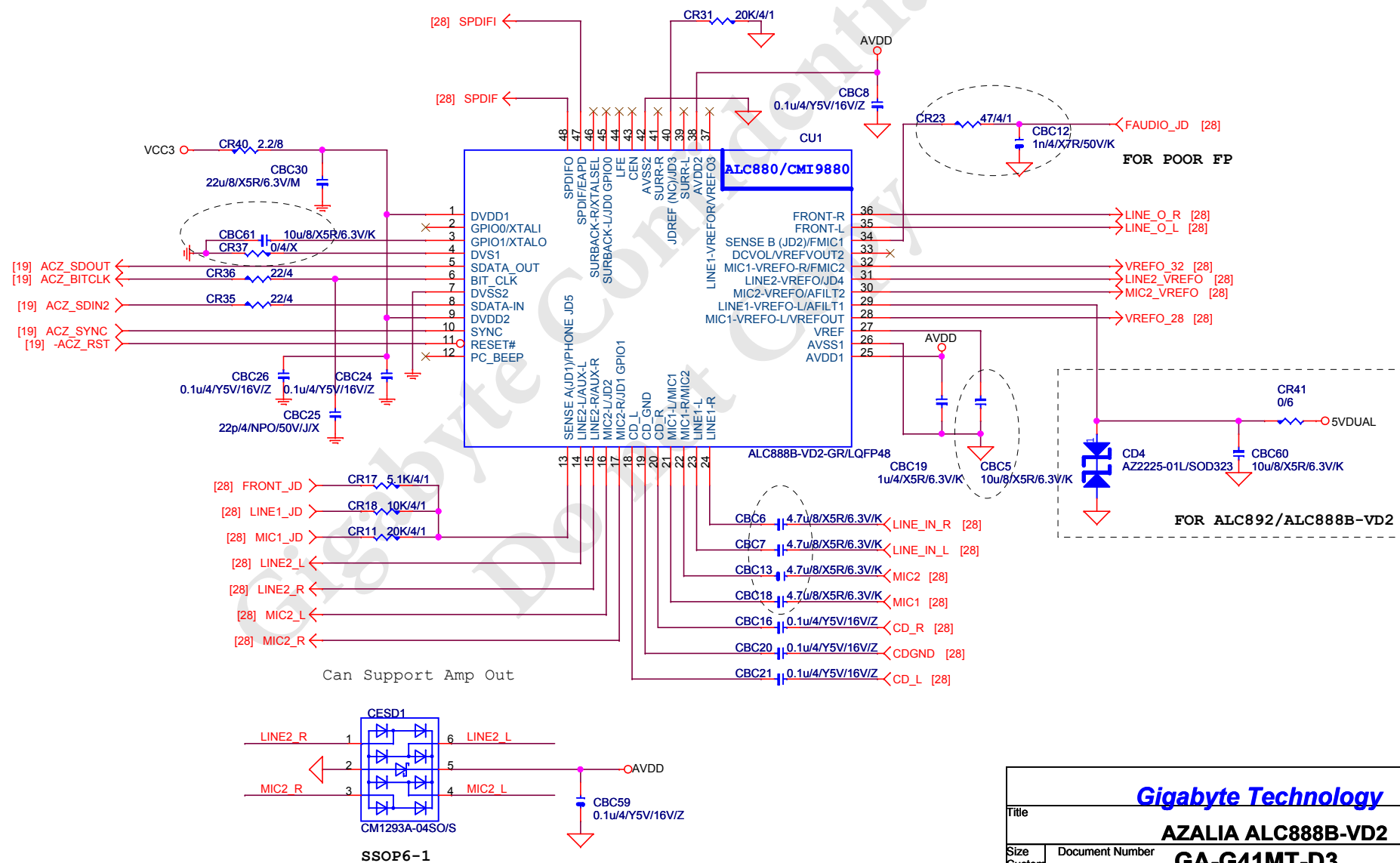


VOLTAGE-- H/W MONITOR

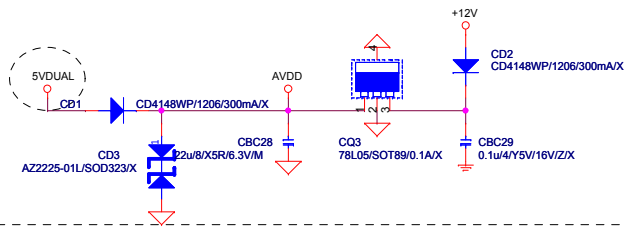


## DUAL BIOS

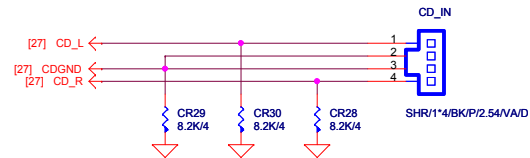




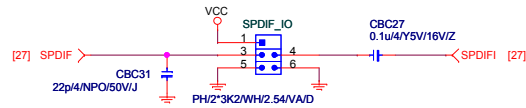
## CODEC POWER/EMI PAD



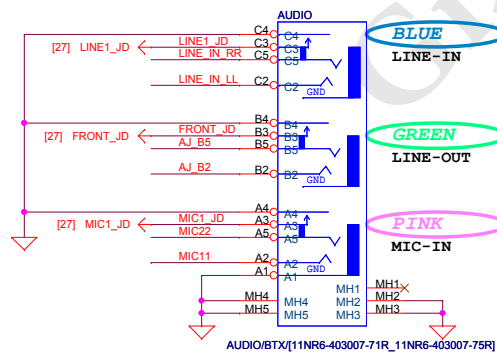
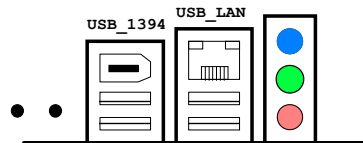
## CD IN



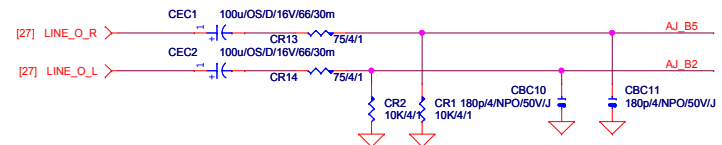
## SPDIF IN



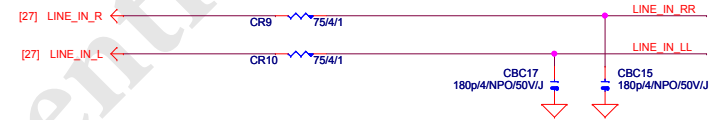
## AZALIA JACK



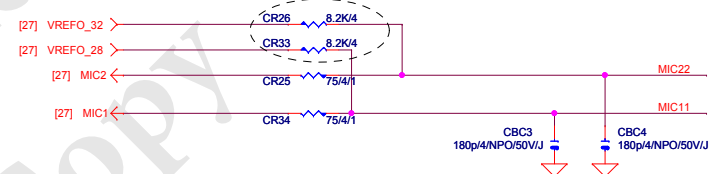
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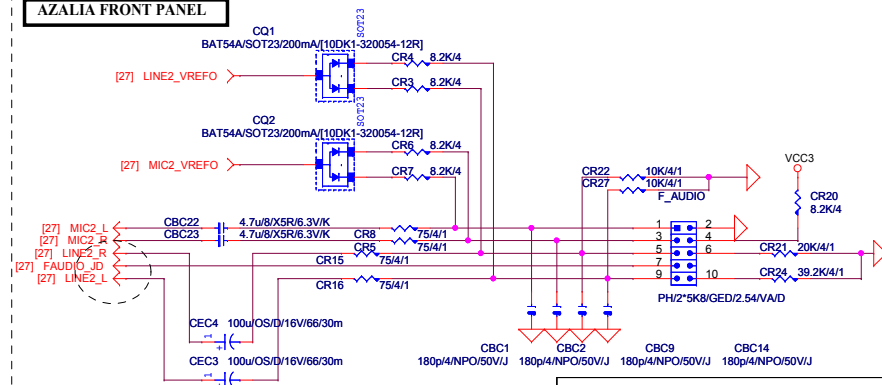
## LINE-IN



## MIC-IN



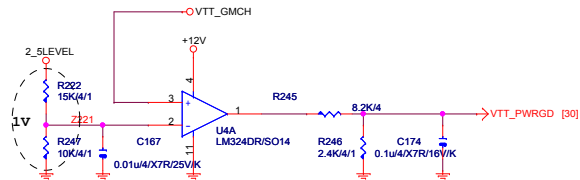
## AZALIA FRONT PANEL



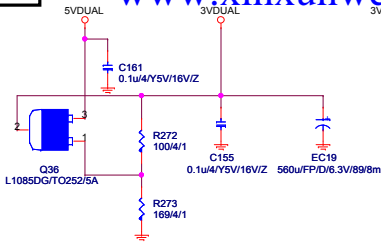
Gigabyte Technology

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AUDIO JACK			
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VTT\_PWRGD

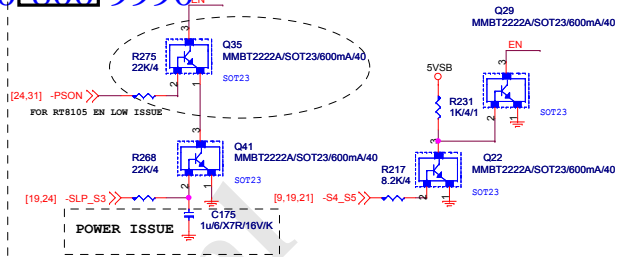


3VDUAL

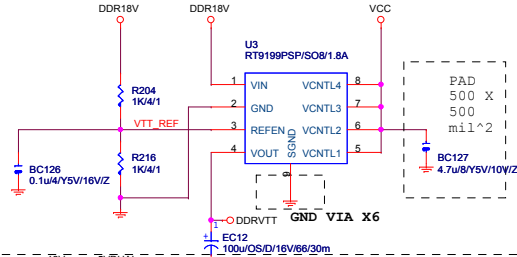


EUP

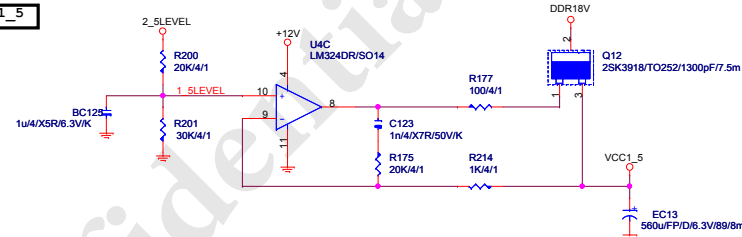
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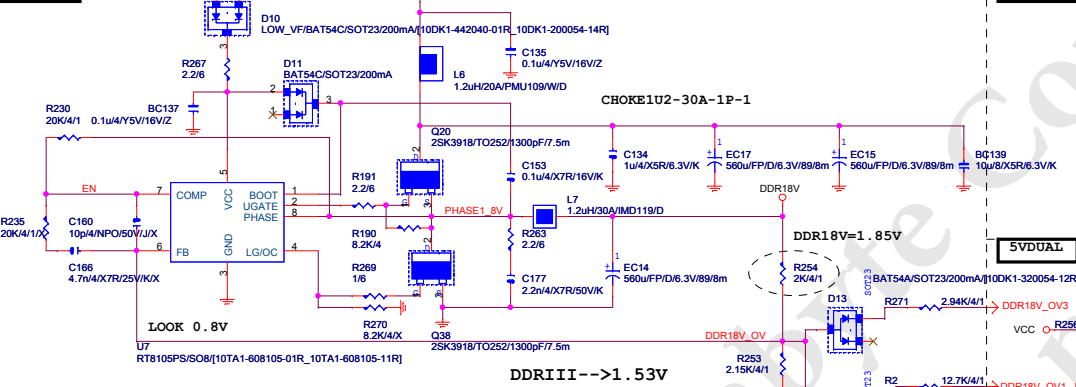
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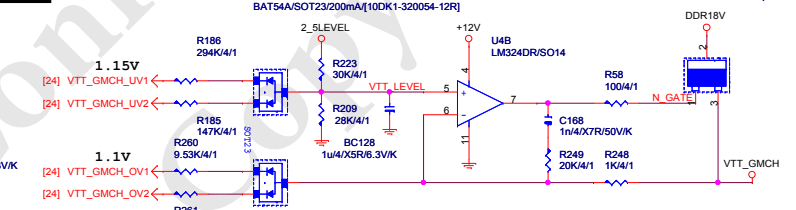
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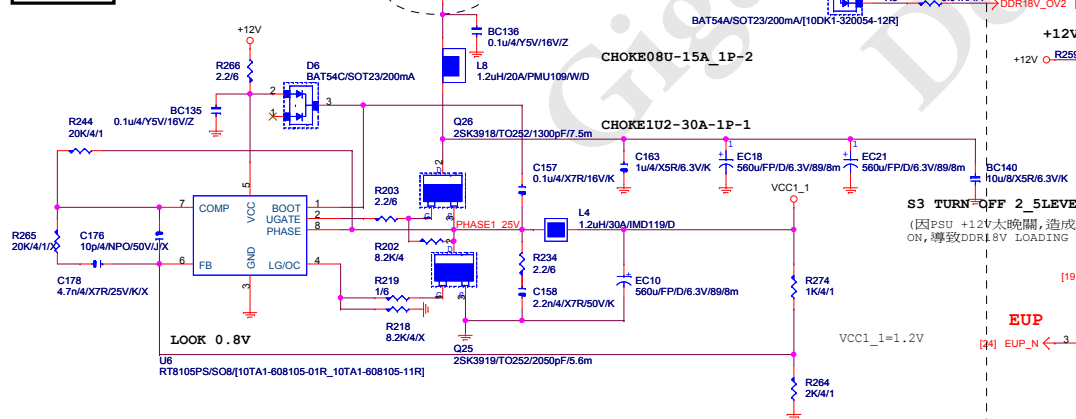
DDR18V



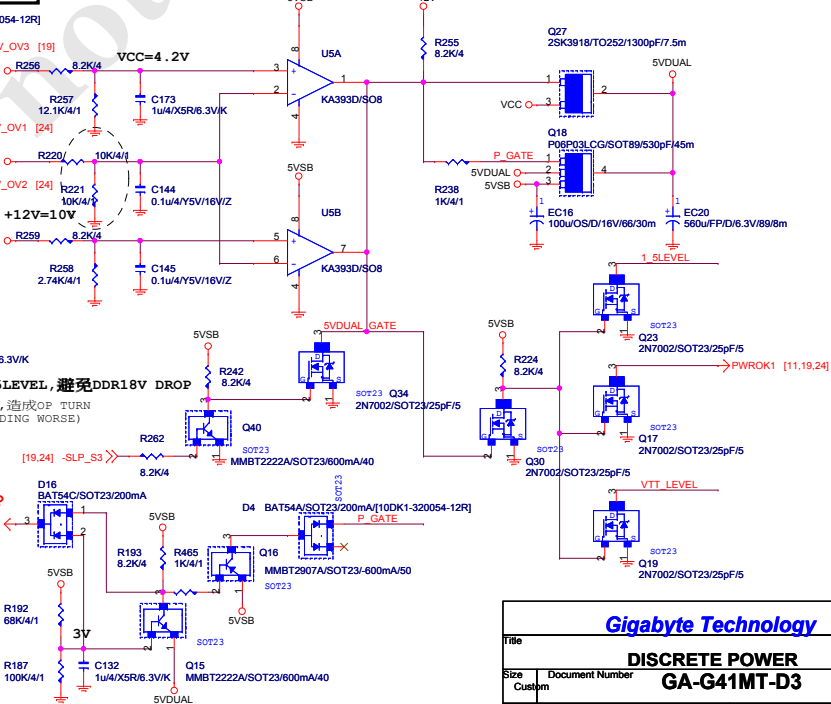
VTT\_GMCH

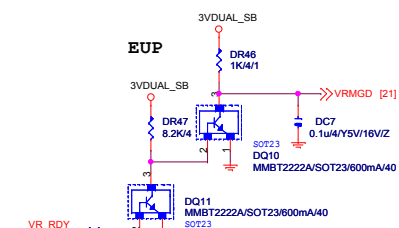
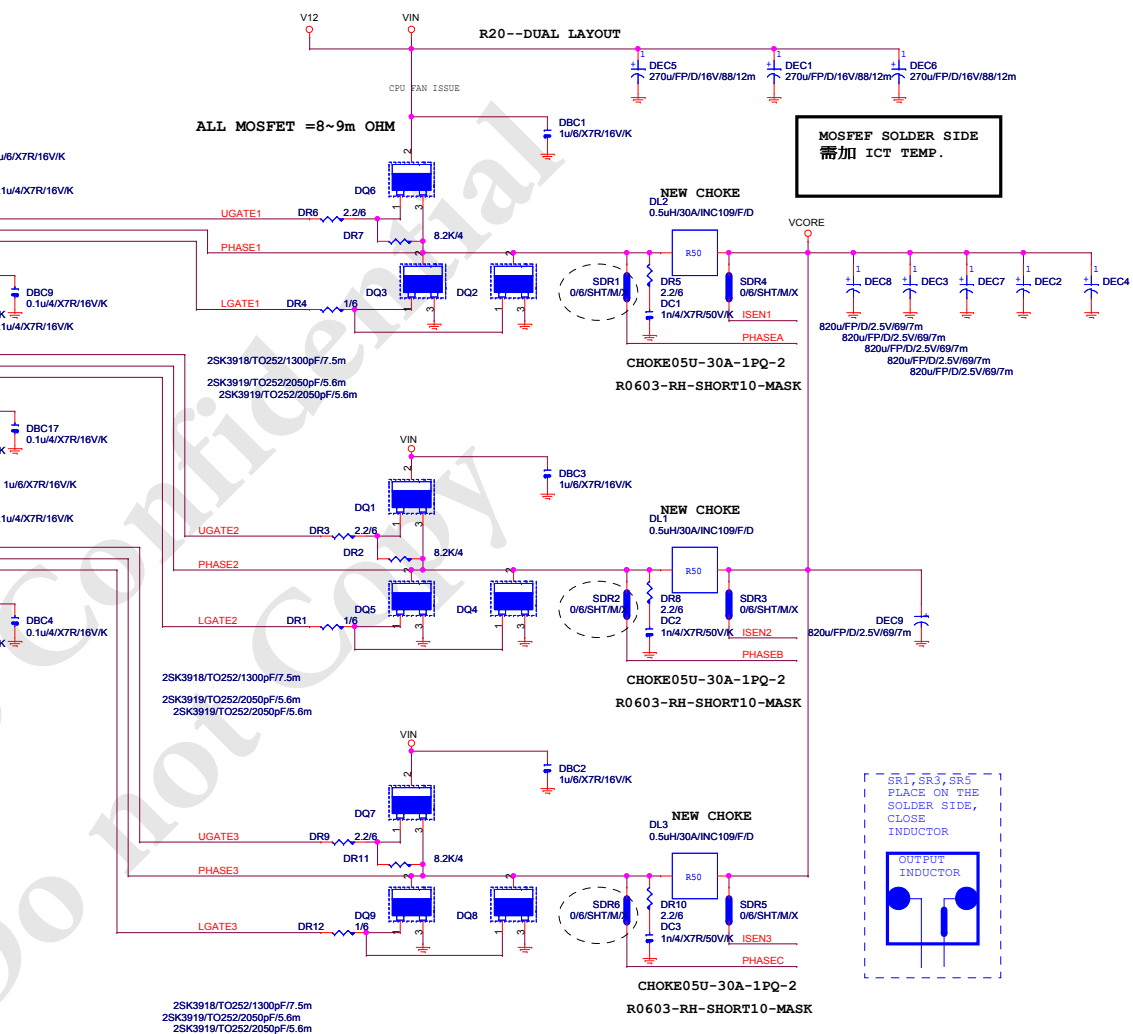
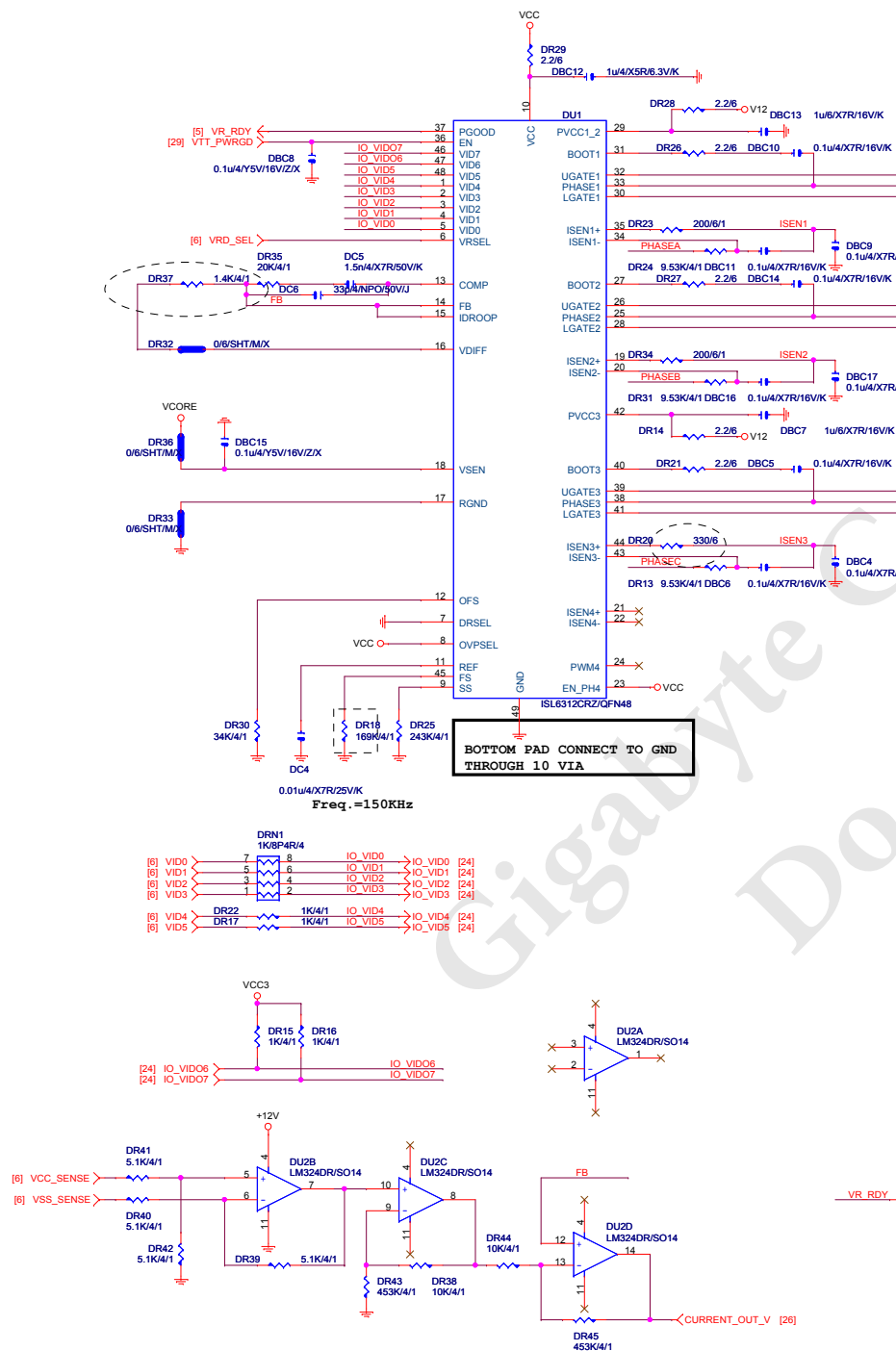


VCC1\_1

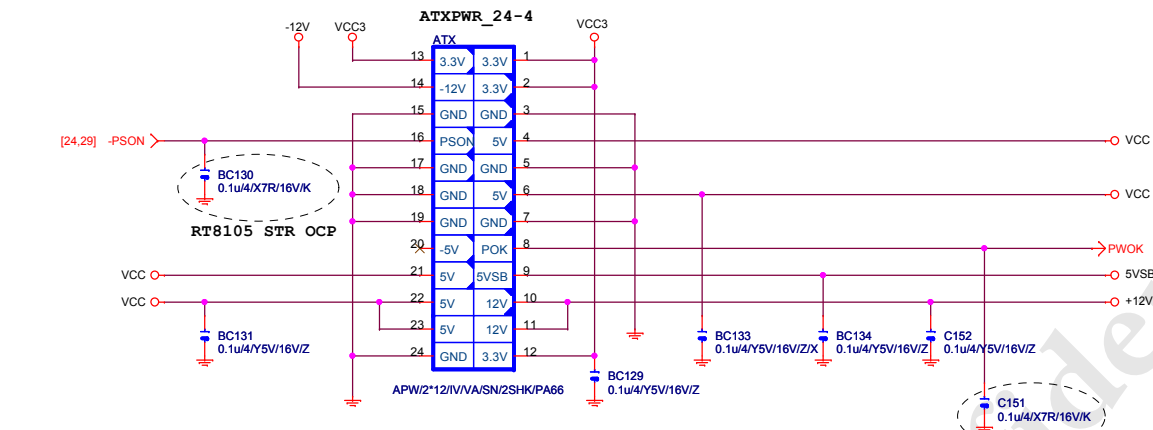


5VDUAL

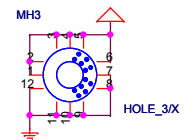
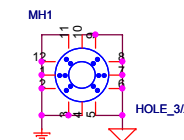
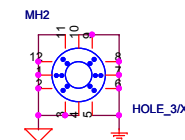
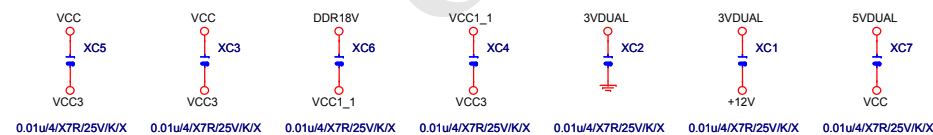
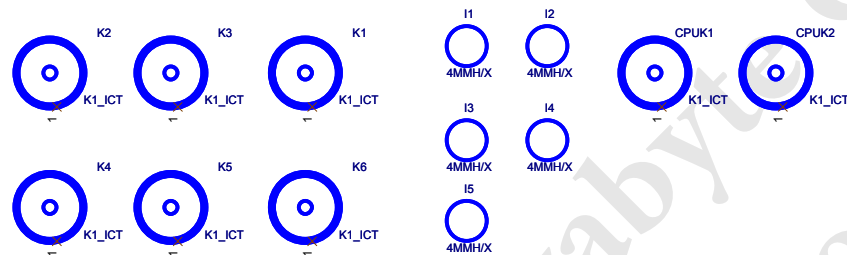
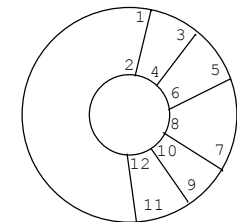
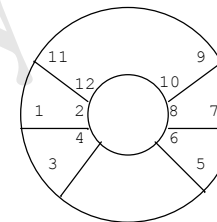
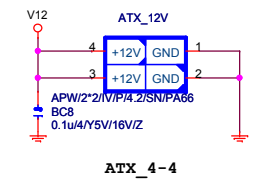
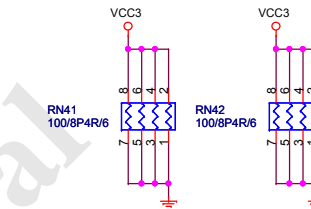




## ATX POWER CONNECTOR

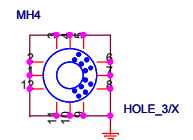
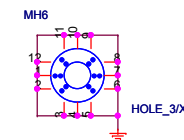
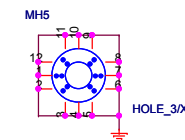


## FIX PWR AcBel (ATX-400C-A2ADB)



HOLE\_4-RH-1

HOLE\_4-RH-5MM-1



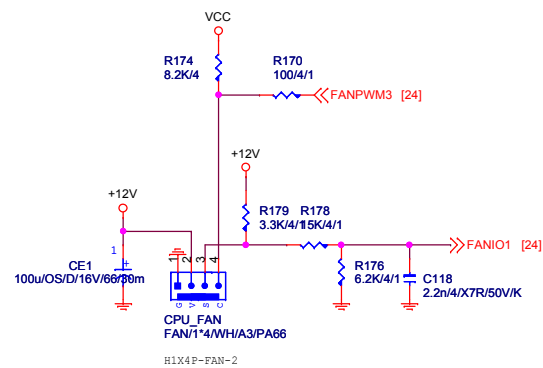
Gigabyte Technology

Title ATX POWER CONNECTOR

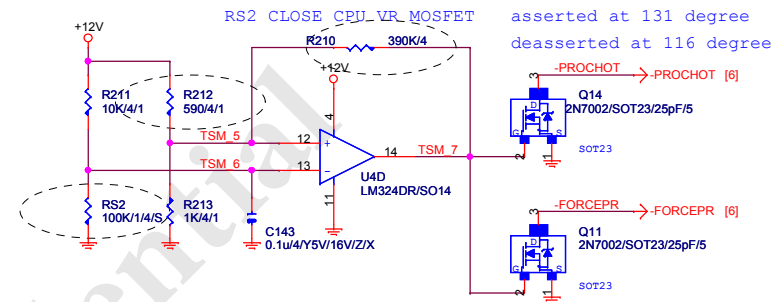
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## CPU SMART FAN SMART FAN

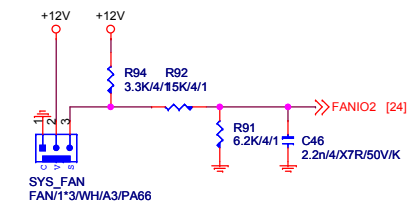
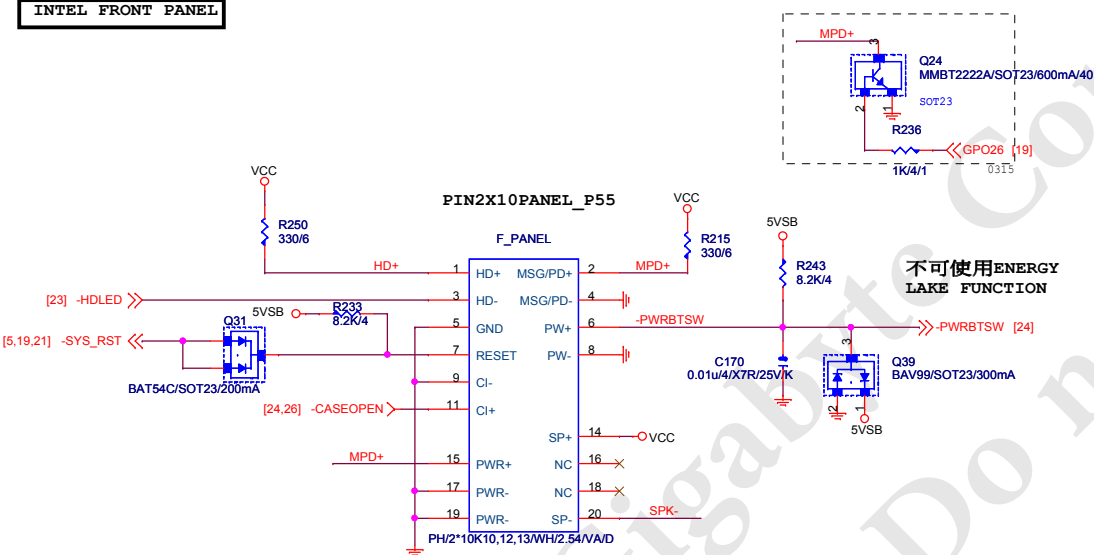


## PROCESSOR HOT

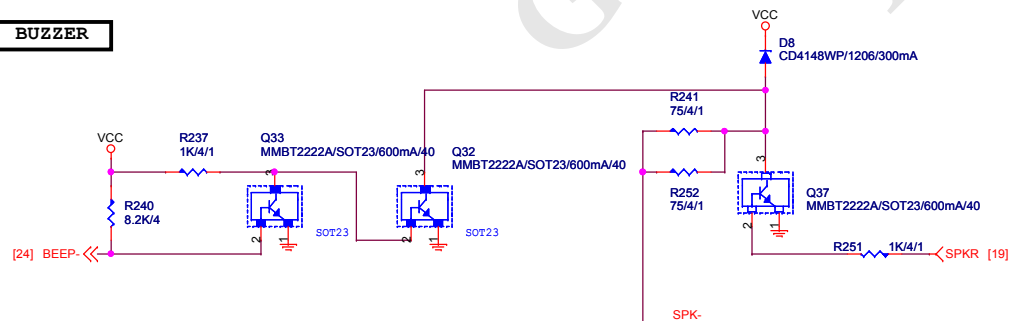


## INTEL FRONT PANEL

## SYS\_FAN



## BUZZER



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FRONT PANEL		
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